

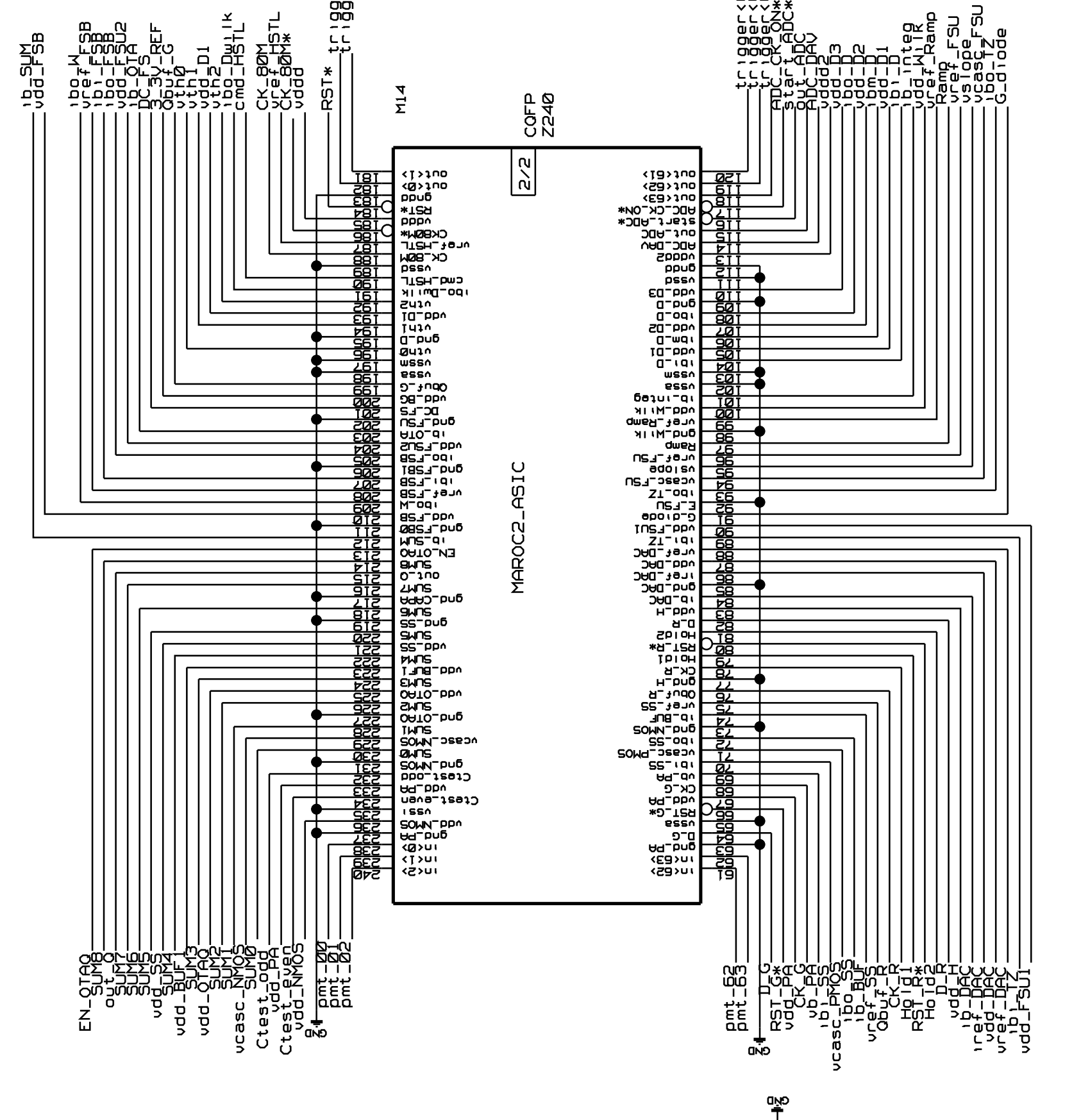
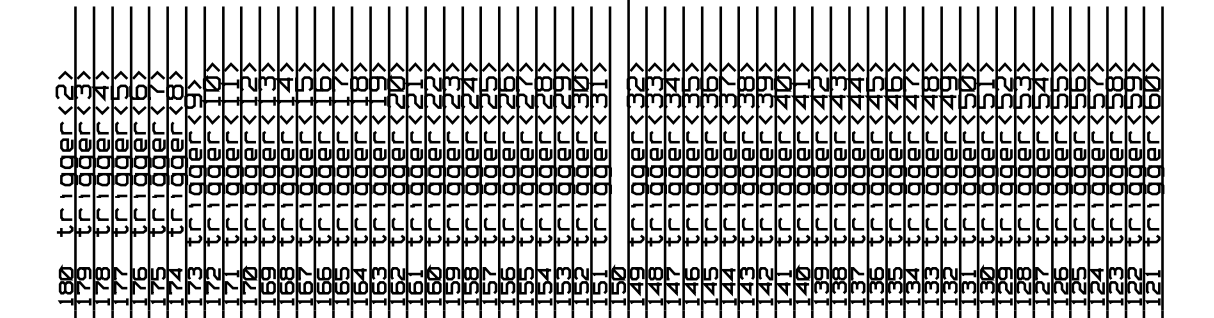
CHIP

- P88 ○ PRSTILLE143 ADC_CK_ONK
- P93 ○ PRSTILLE143 start_ADC*
- P95 ○ PRSTILLE143 out_ADC
- P108 ○ PRSTILLE143 ADC_DAV

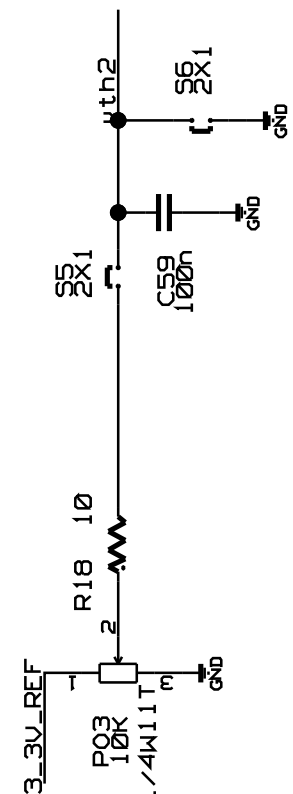
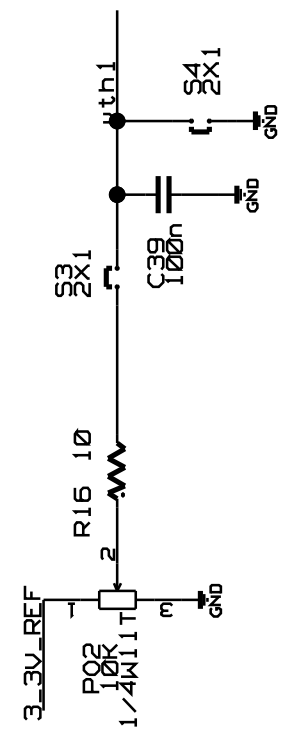
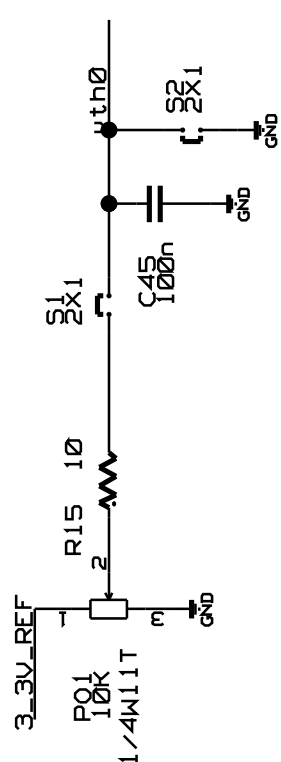
- P57 ○ PRSTILLE143 trigger<0>
- P58 ○ PRSTILLE143 trigger<8>
- P59 ○ PRSTILLE143 trigger<24>
- P61 ○ PRSTILLE143 trigger<40>
- P62 ○ PRSTILLE143 trigger<47>

Z240
COFP

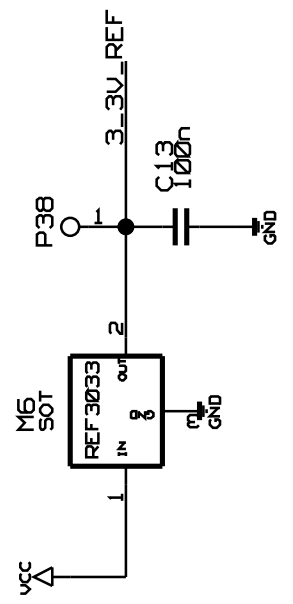
M14




Threshold

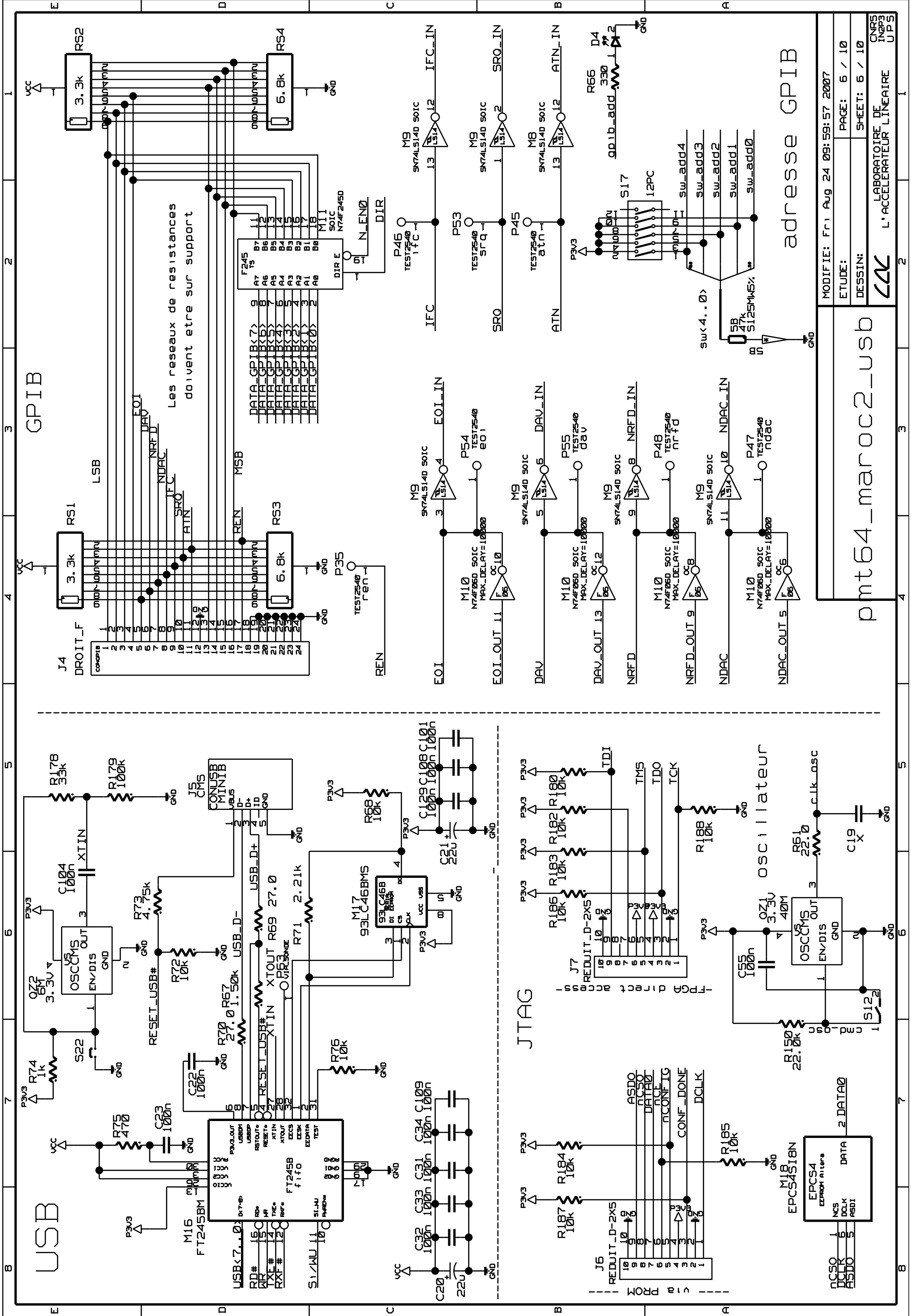


TENSIONS DE REFERENCE



pmt64_maroc2_usb

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 LABORATOIRE DE INEP3 L'ACCELERATEUR LINEAIRE UPS	



USB

GPIB

Les reseaux de resistances
doivent etre sur support

adresse GPIB

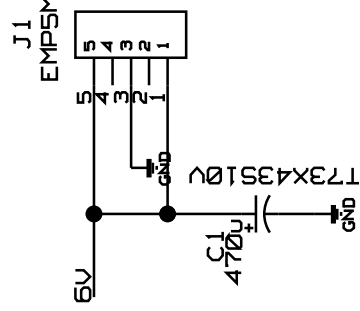
MODIFIE: Fri Aug 24 09:59:57 2007
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LABORATOIRE DE L'ACCELERATEUR LINEAIRE
INAP3 UPS

pmt64_maroc2_usb

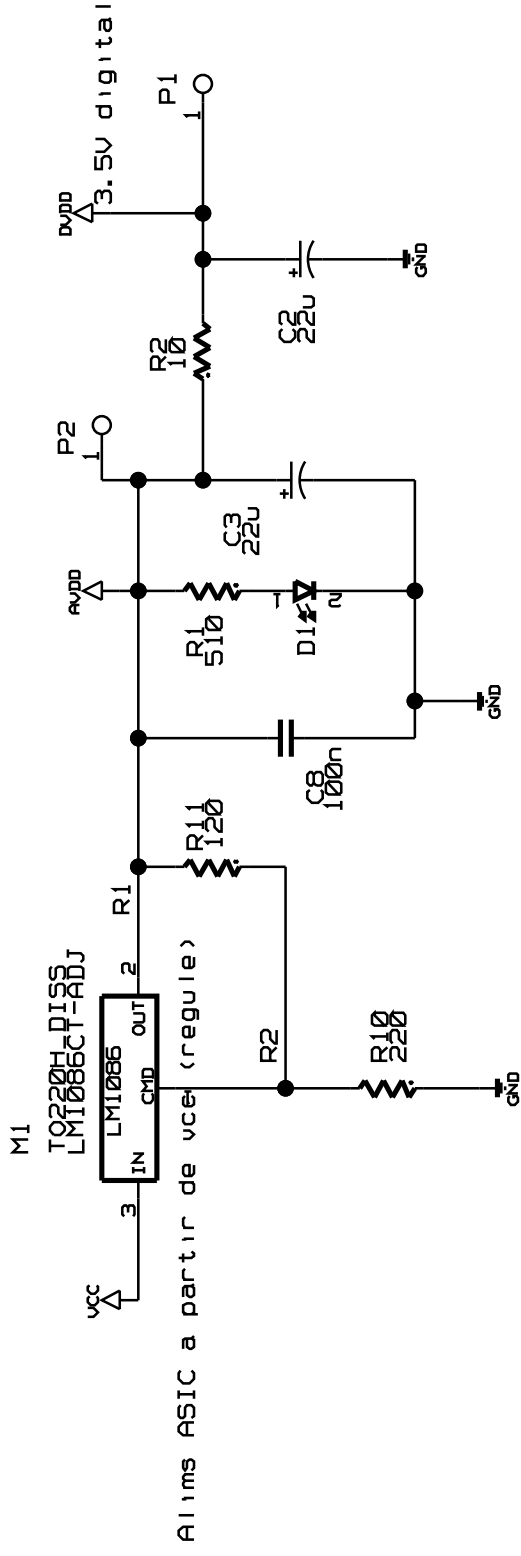
CAK

POWER SUPPLY

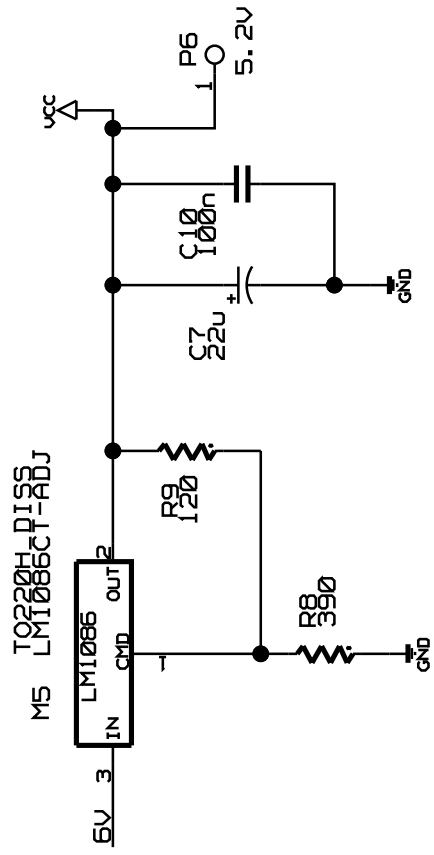
R 0805 a 1% pour les regulateurs
 $V_{reg} = 1.25 * (1 + R2/R1)$ et $R1 = 240$ environ



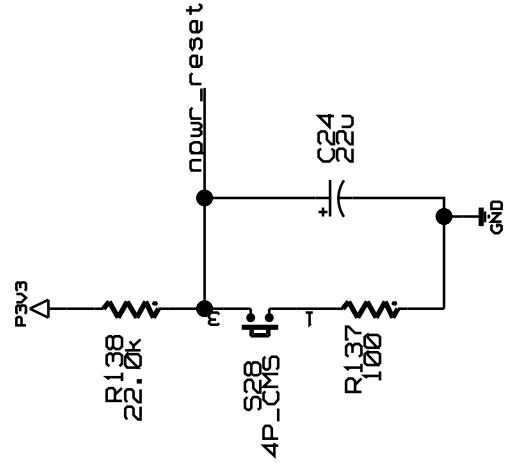
alimentation ASIC



VCC pour ADC et GPIB

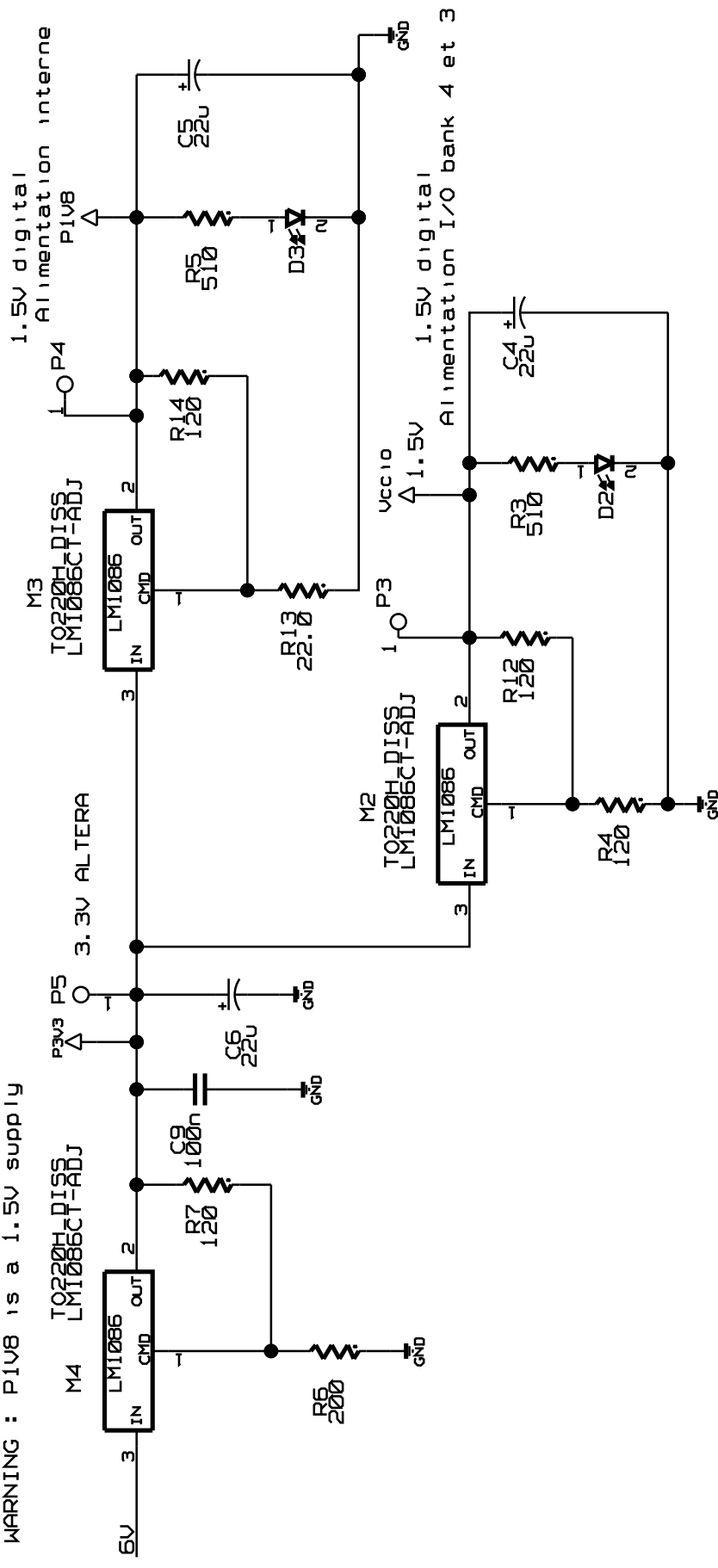


Power-on reset



alimentation pour ALTERA

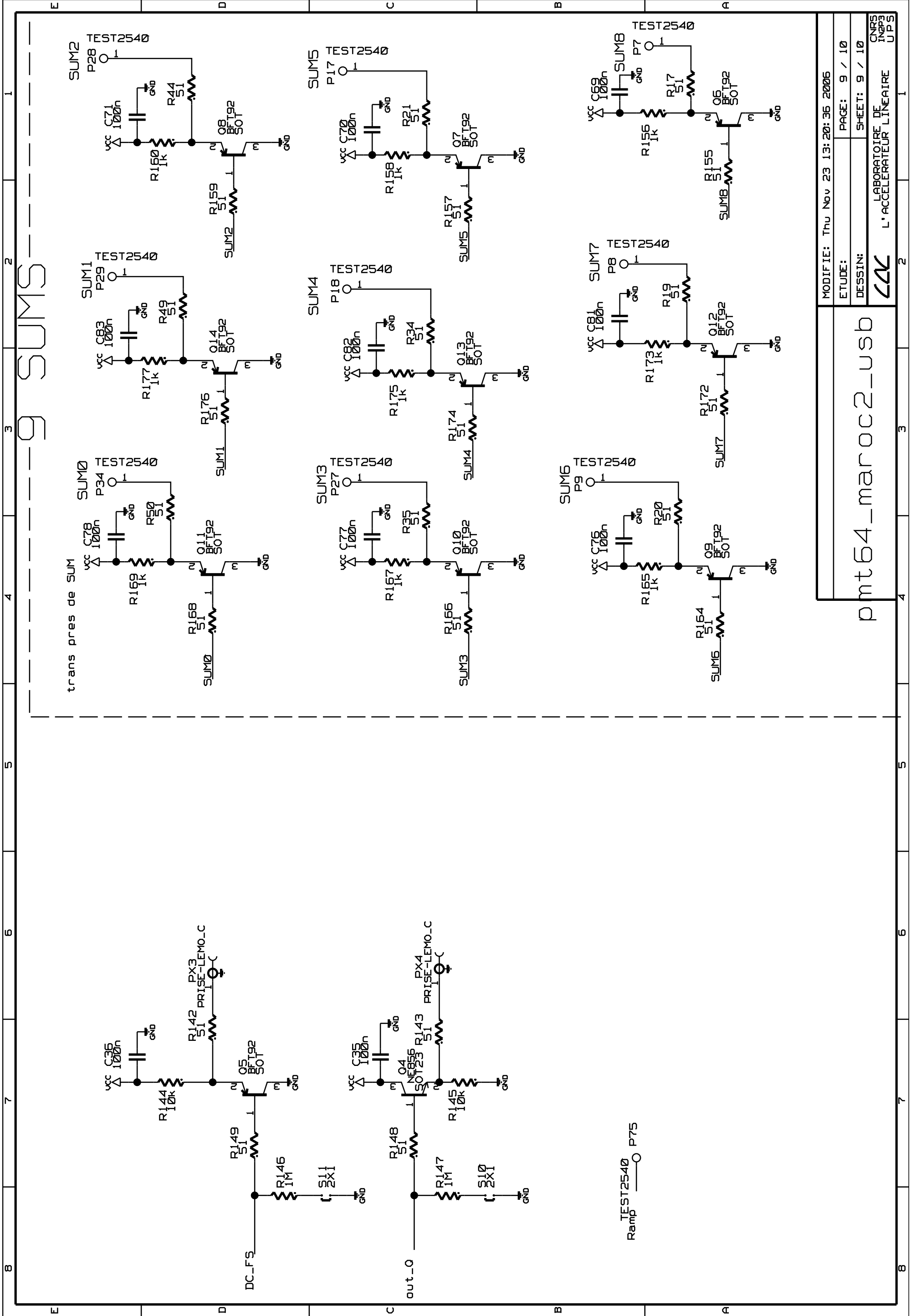
WARNING : P1V8 is a 1.5V supply



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DESSIN:	

pmt64_maroc2_usb

LABORATOIRE DE
 L'ACCELERATEUR LINEAIRE
 CNRS
 IN2P3
 UPS

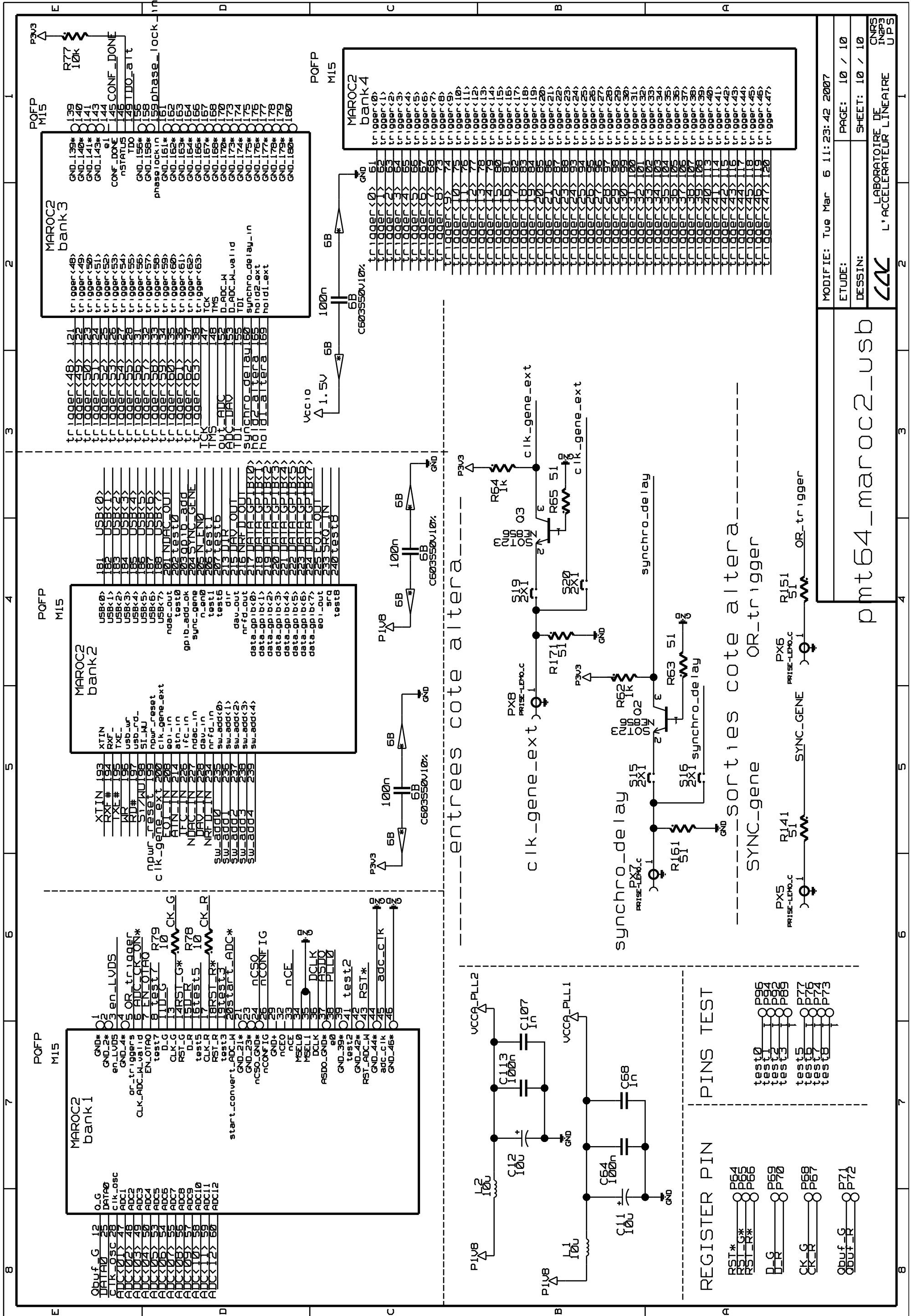


SUMS

trans pres de SUM

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 LABORATOIRE DE INAP3 L'ACCELERATEUR LINEAIRE UPS

pmt64_maroc2_usb



MAROC2 bank 1

Qbuf_G	12
DATA0	25
CLK_OSC	28
ADC1	47
ADC2	48
ADC3	49
ADC4	50
ADC5	51
ADC6	52
ADC7	53
ADC8	54
ADC9	55
ADC10	56
ADC11	57
ADC12	58
ADC13	59
ADC14	60

MAROC2 bank 2

XTIN	193
RXF	194
TXE	195
NR	196
SI7	197
SI8	198
SI9	199
SI10	200
SI11	201
SI12	202
SI13	203
SI14	204
SI15	205
SI16	206
SI17	207
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SI19	209
SI20	210
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SI22	212
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SI28	218
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SI39	229
SI40	230
SI41	231
SI42	232
SI43	233
SI44	234
SI45	235
SI46	236
SI47	237
SI48	238
SI49	239
SI50	240

MAROC2 bank 3

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