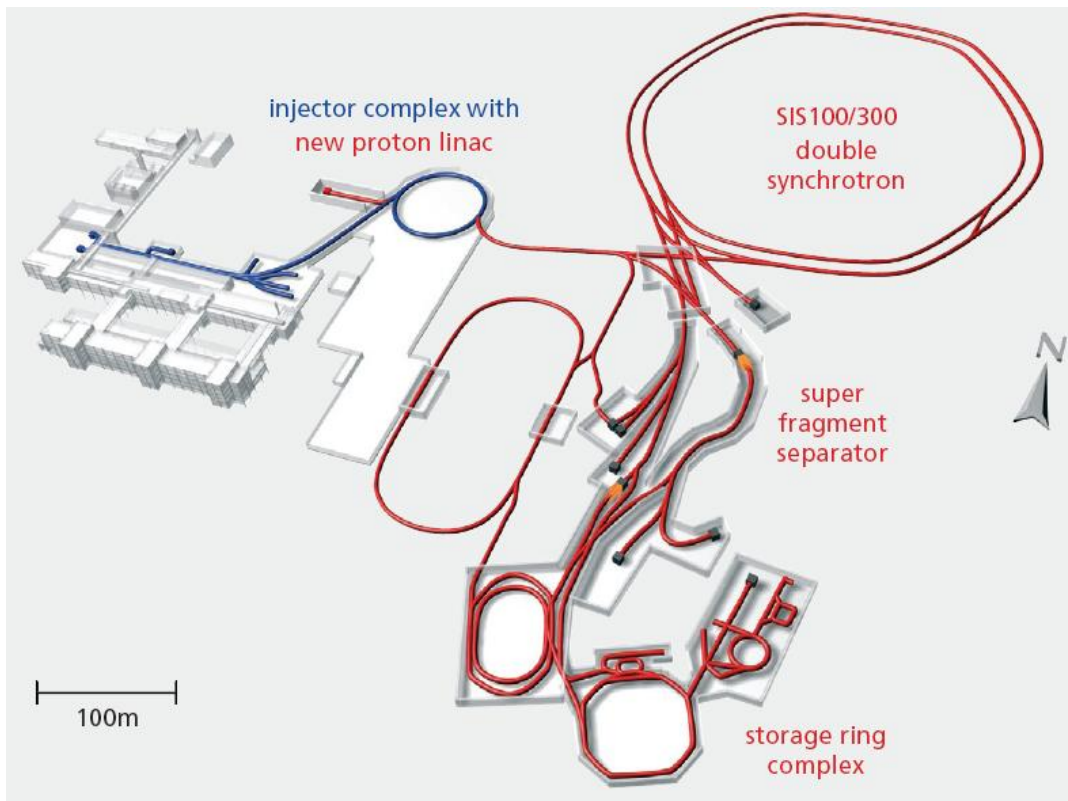




AIDA : a 16-Channel Amplifier ASIC to Read Out the Advanced Implantation Detector Array for Experiments in Nuclear Decay Spectroscopy

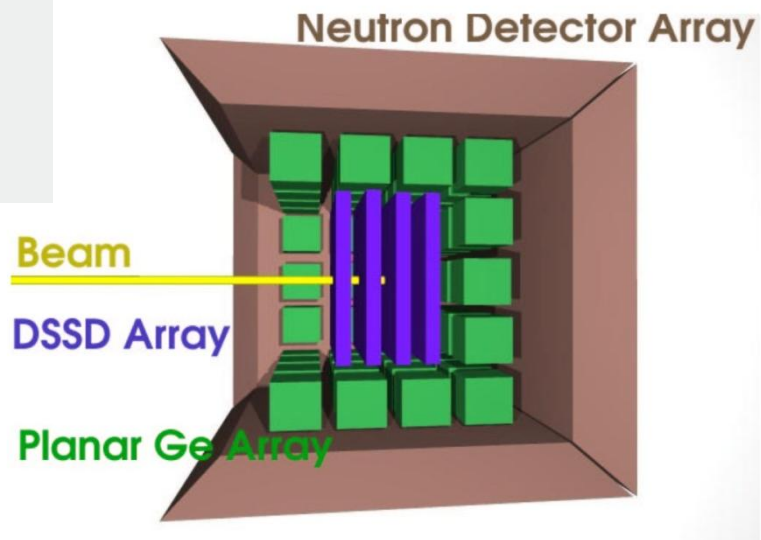
Daive Braga
Stephen Thomas

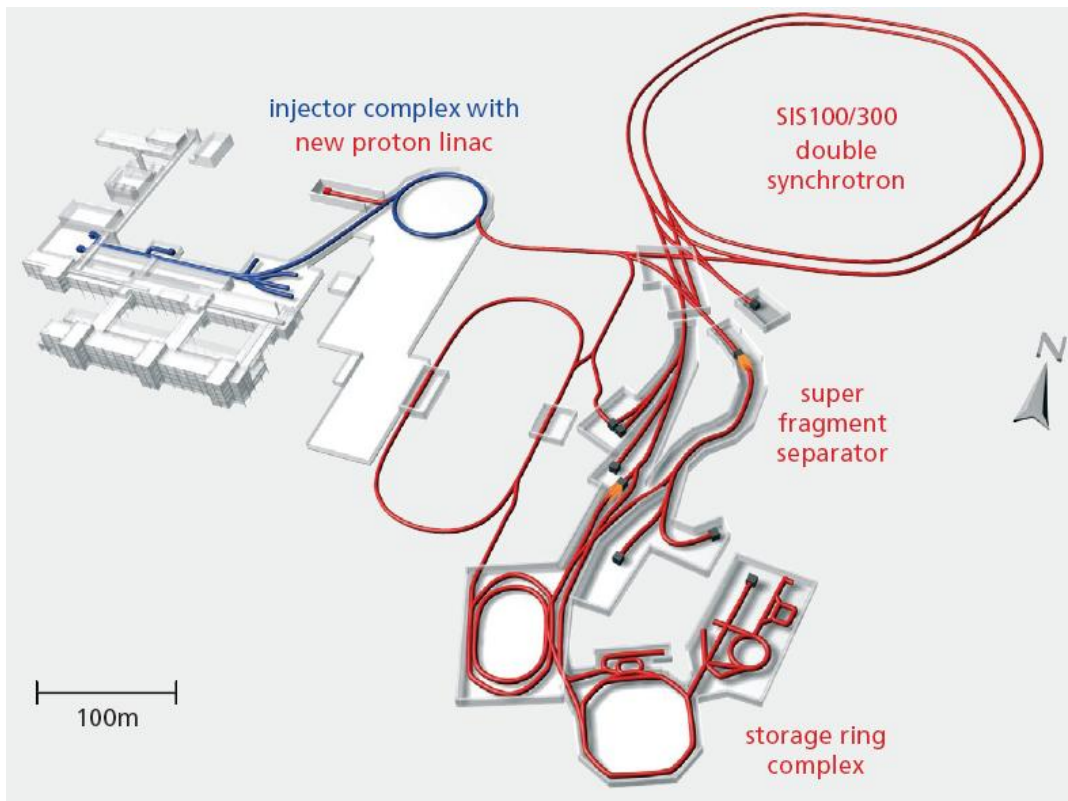
Rutherford Appleton Laboratory, STFC



FAIR, GSI
(Facility for Antiproton and Ion Research)

DESPEC
(DEcay SPECTroscopy)



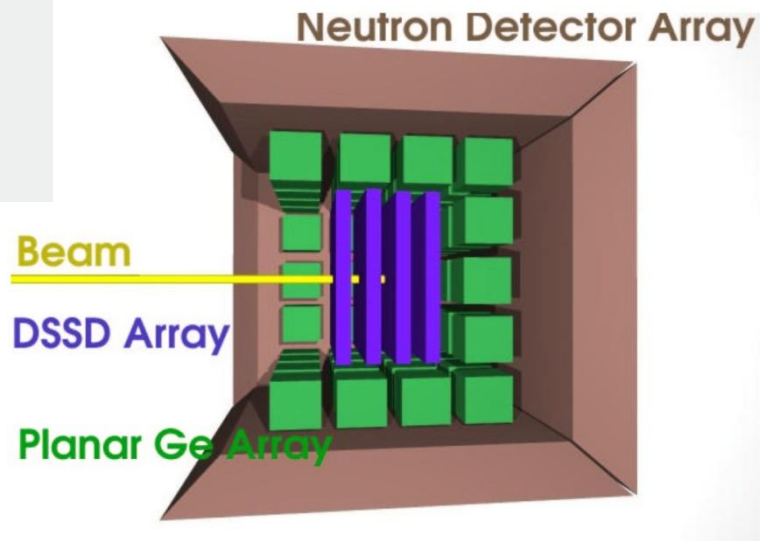


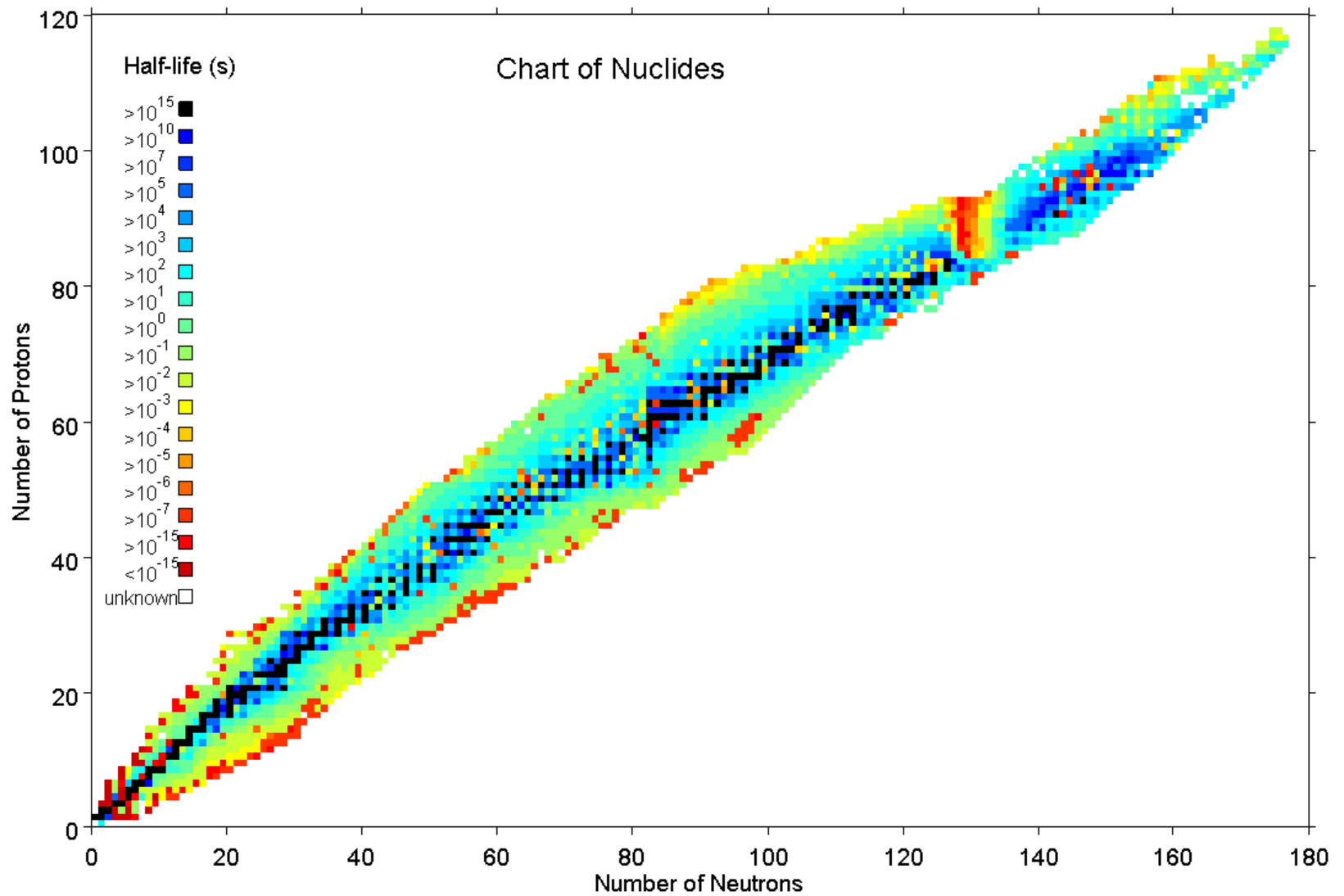
FAIR, GSI
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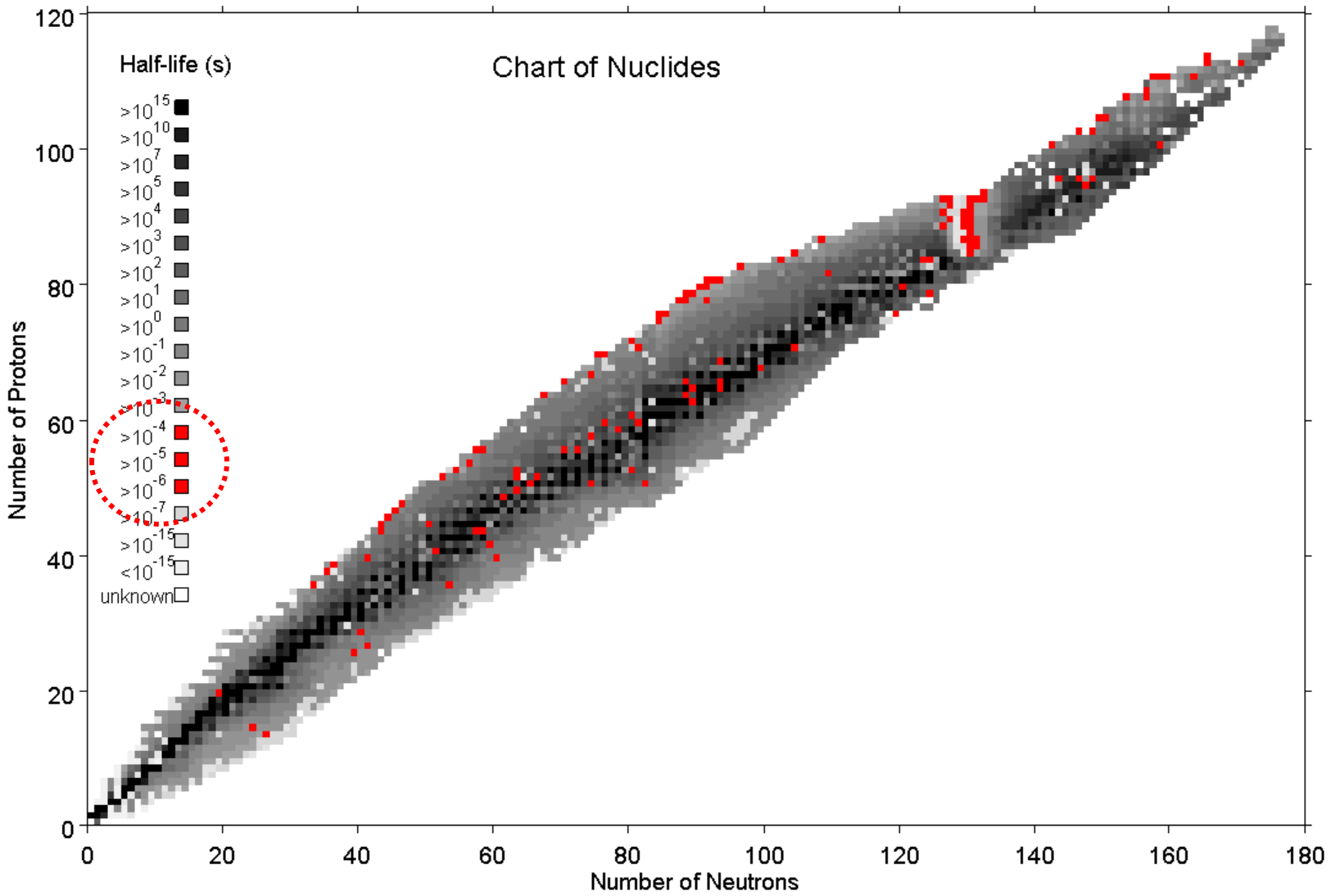
High Energy Implant:
 $\leq 20 \text{ GeV}$

Low-Energy Decay:
 $\geq 25 \text{ KeV}$

DESPEC
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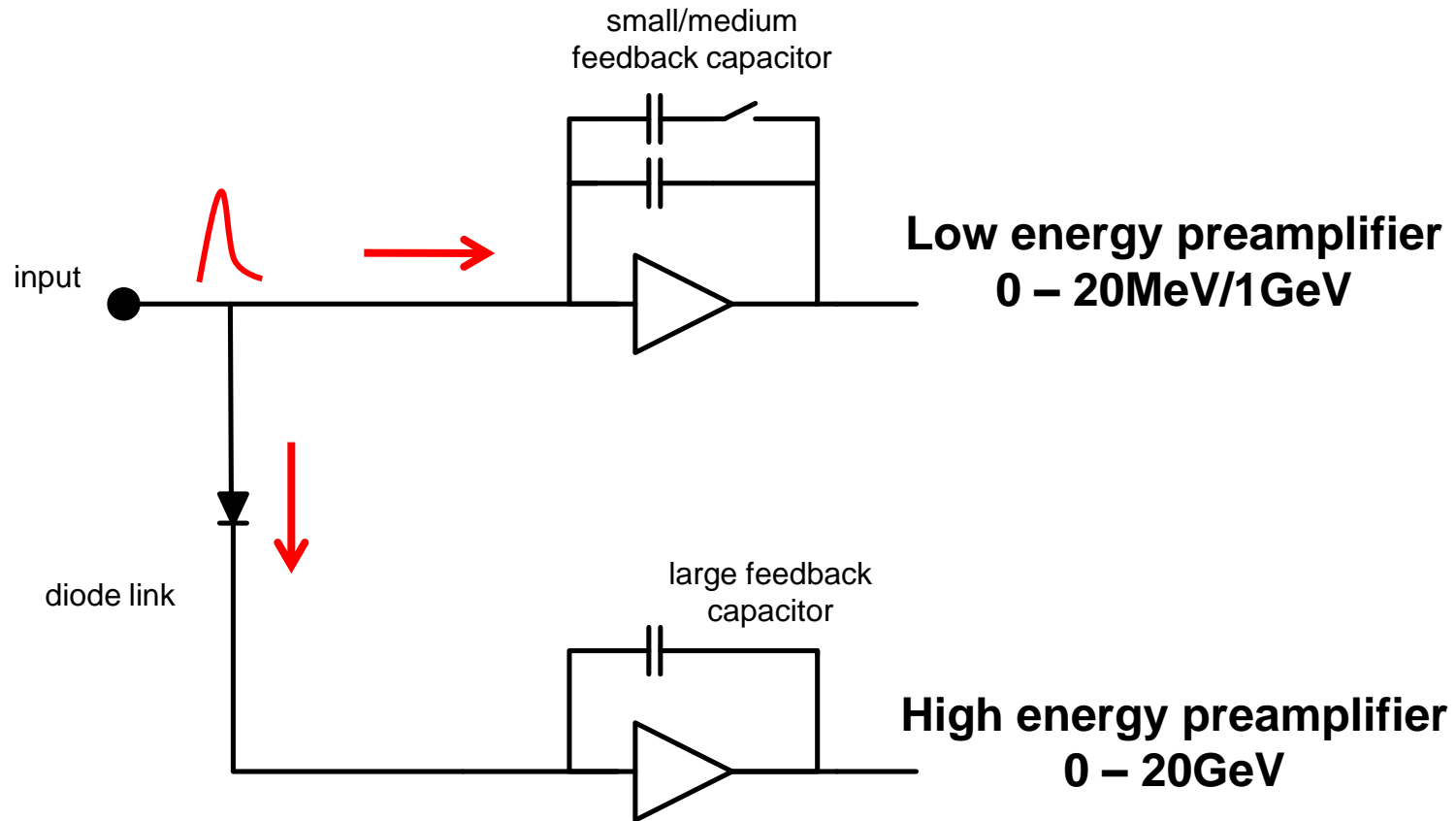




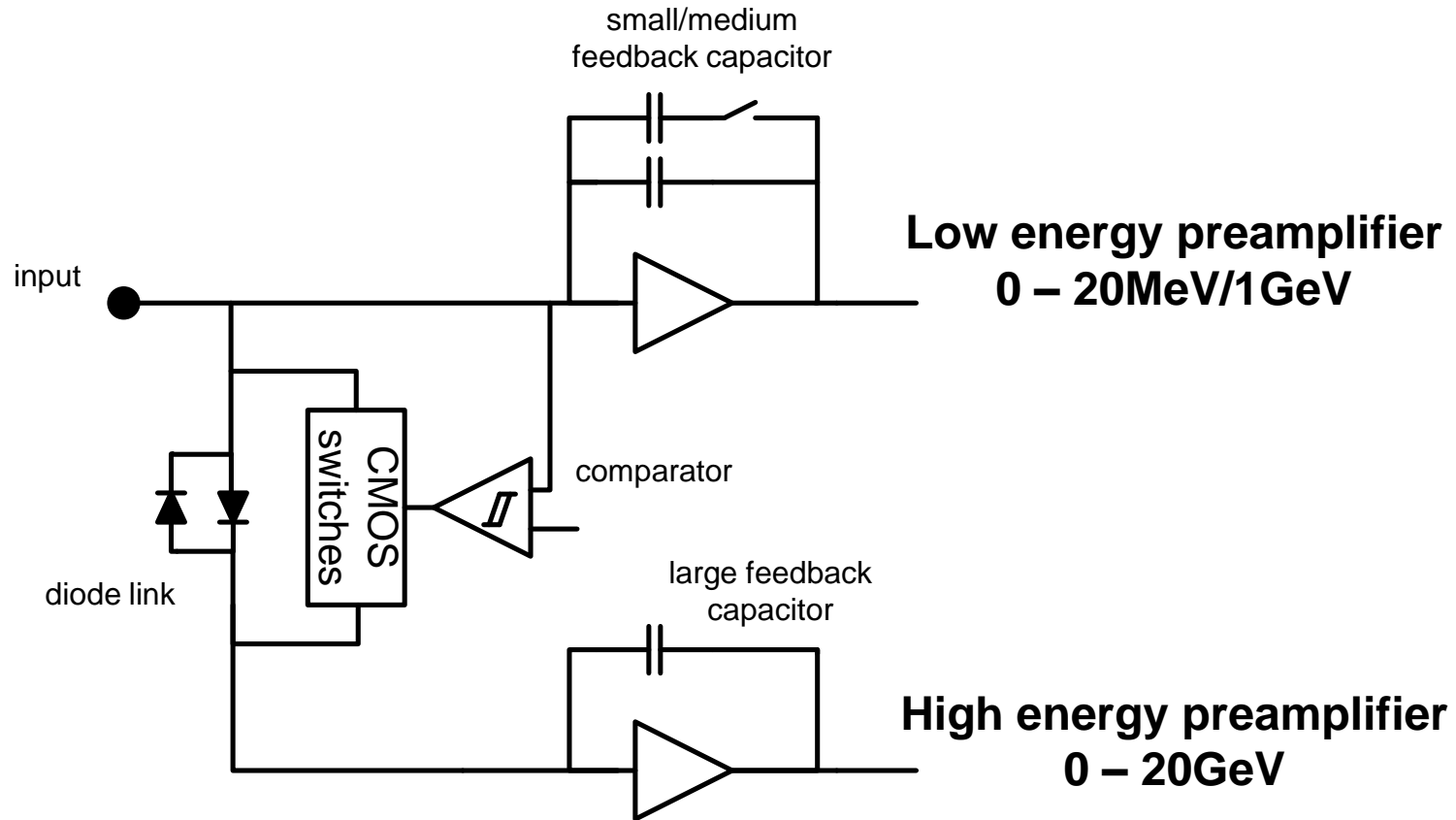
Specifications

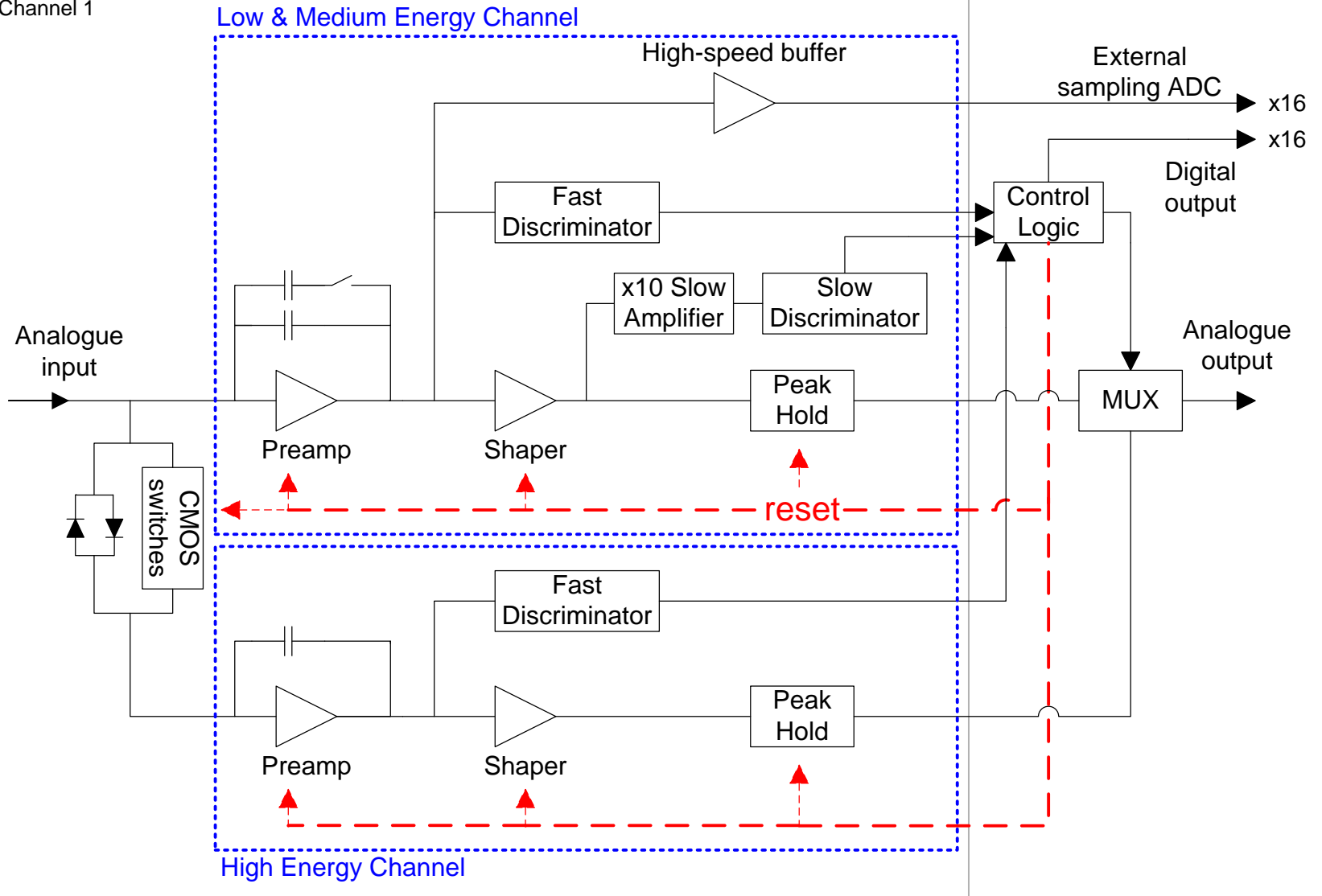
- Input range: 20GeV \rightarrow 25KeV ($\sim 10^6$:1)
- Short separation between high and low energies events ($< 10\mu\text{s}$)
- Integral non linearity $< 0.1\%$
- Autonomous overload detection and recovery
- Input referred noise 5KeV

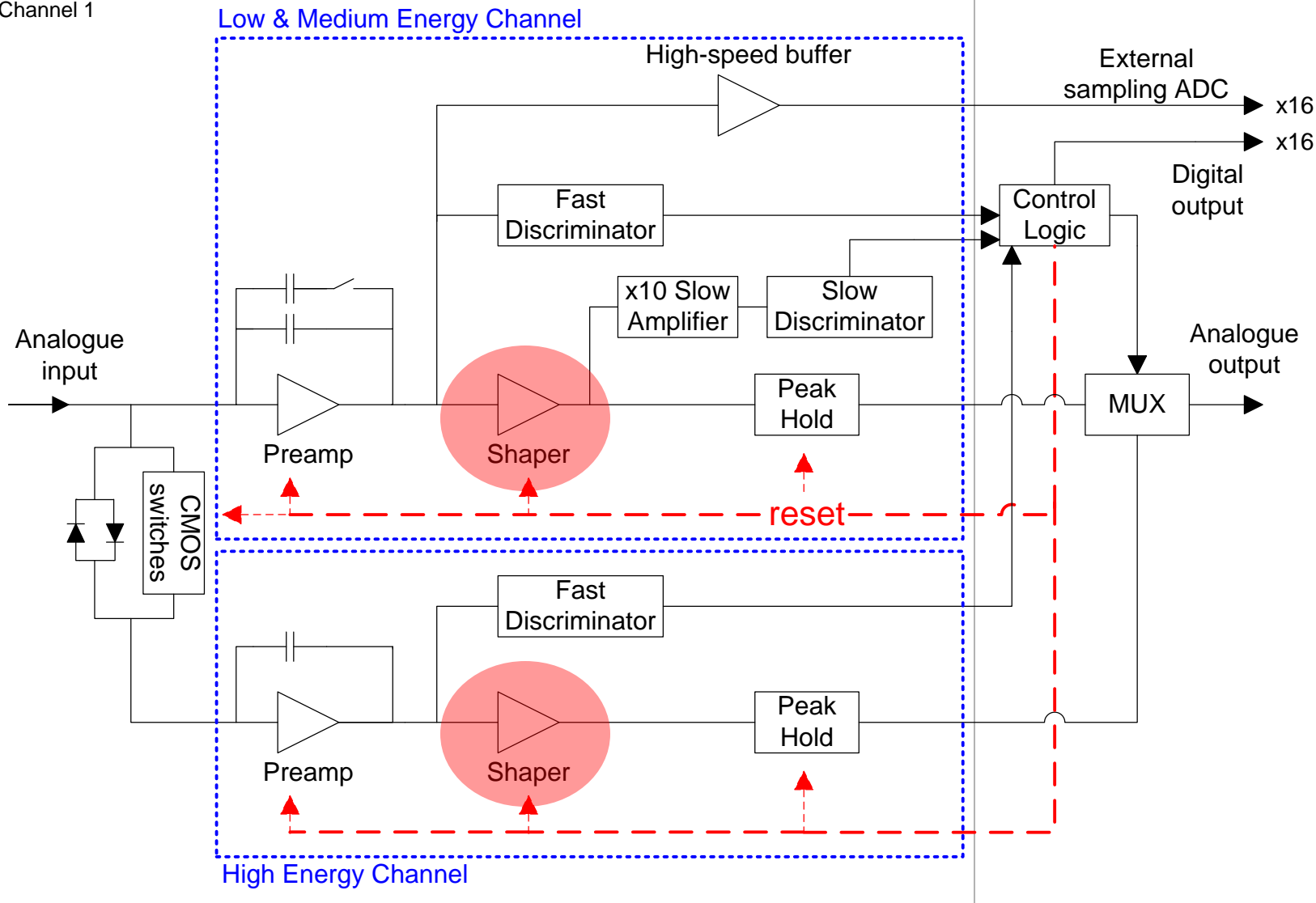
Front end

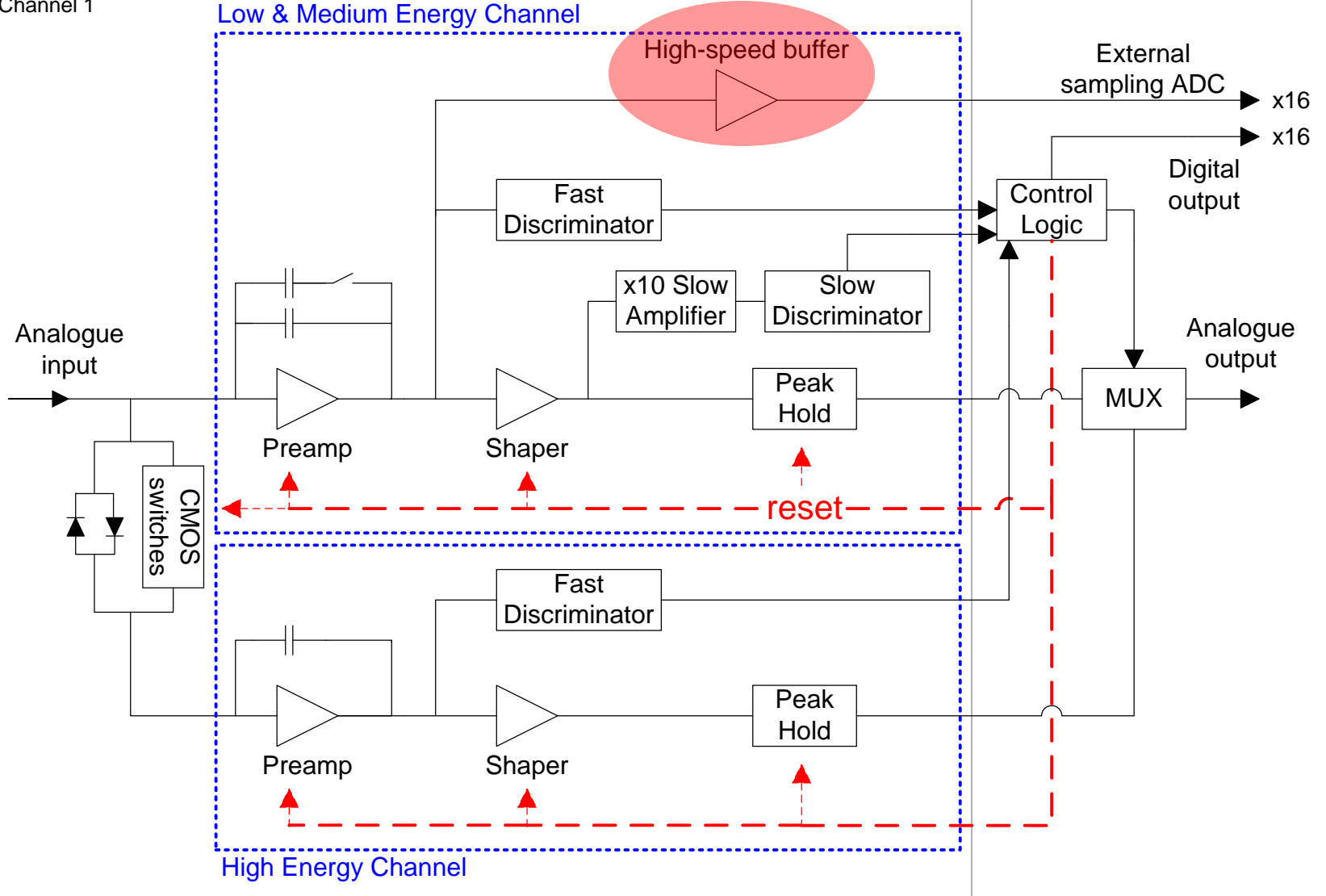


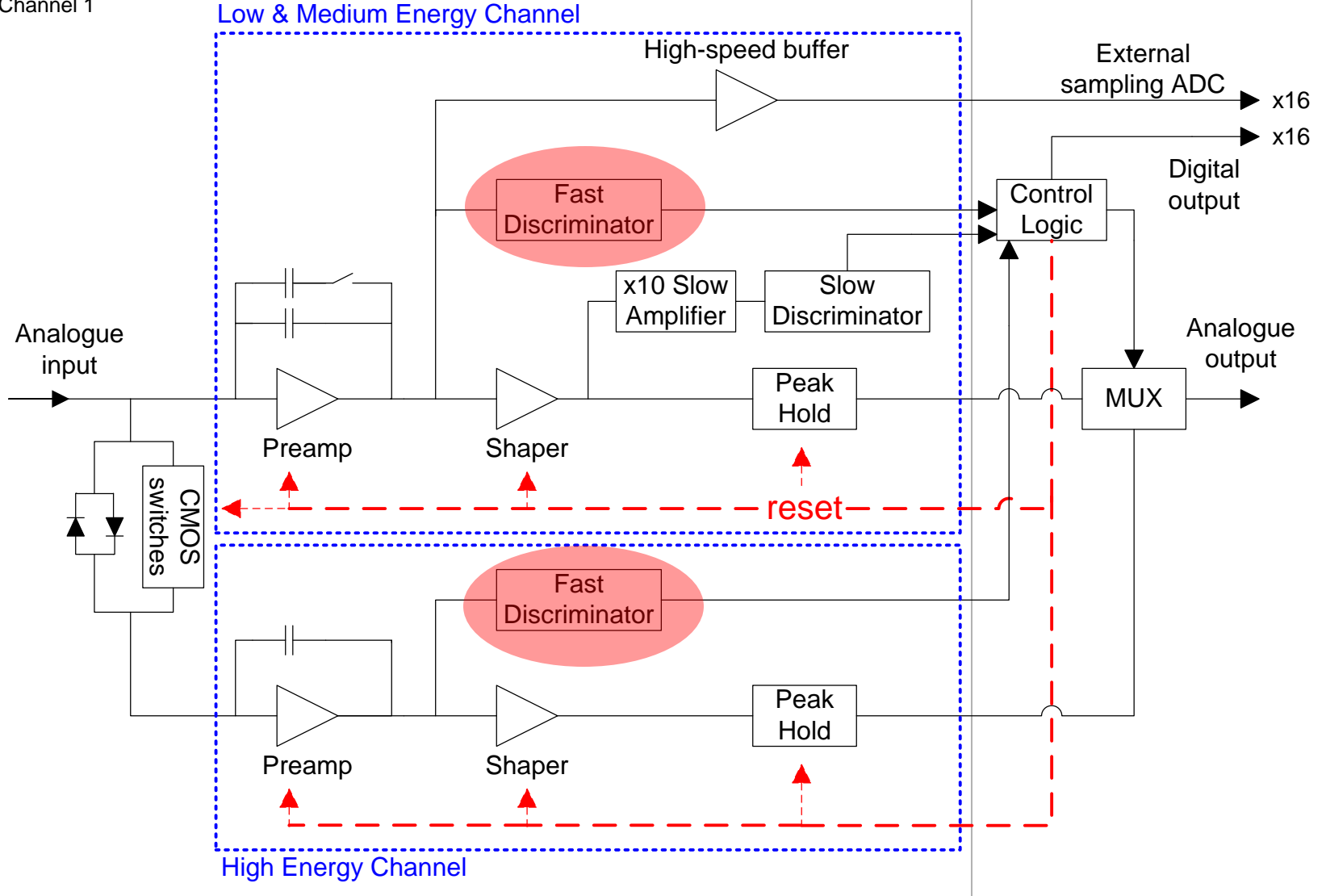
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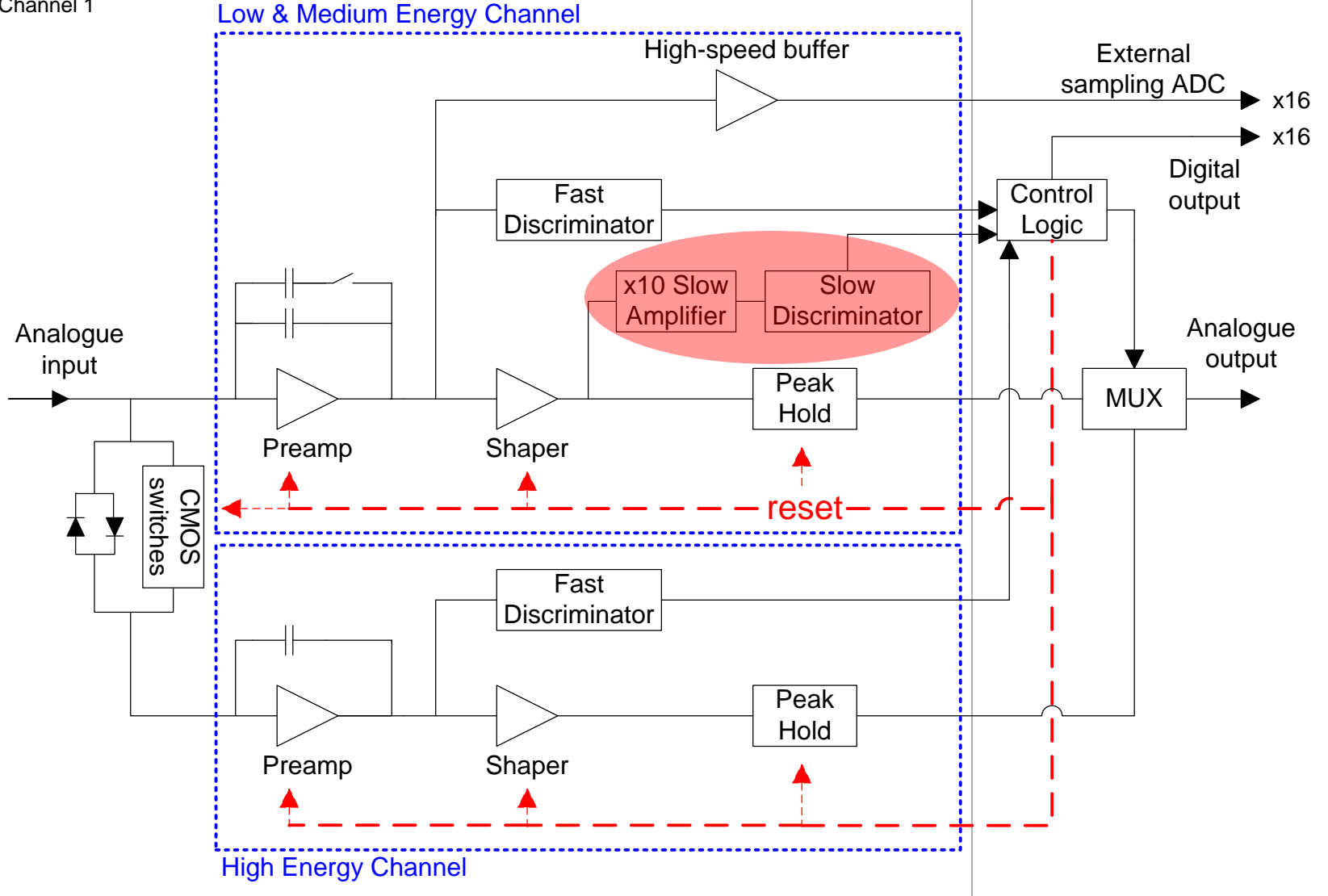


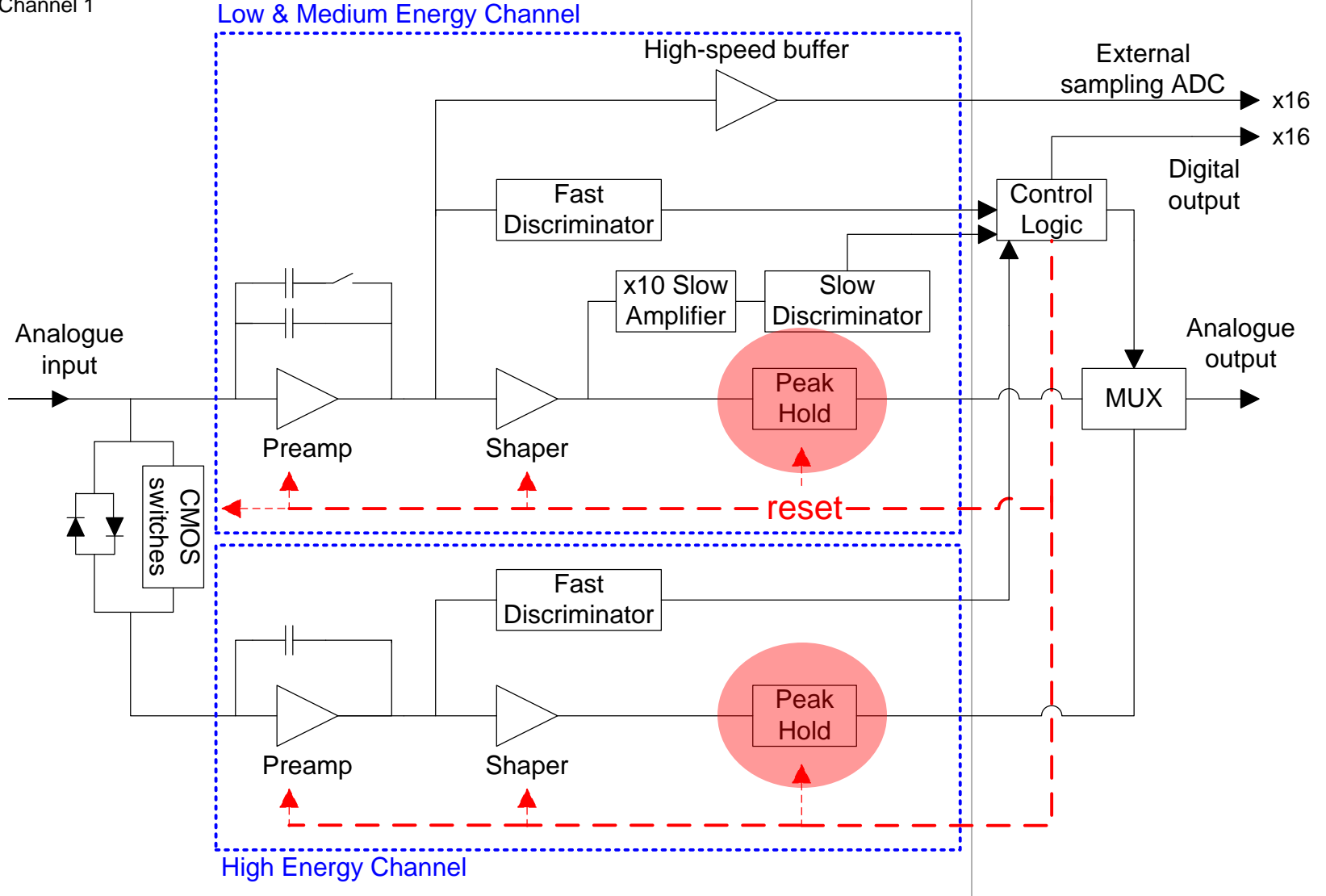


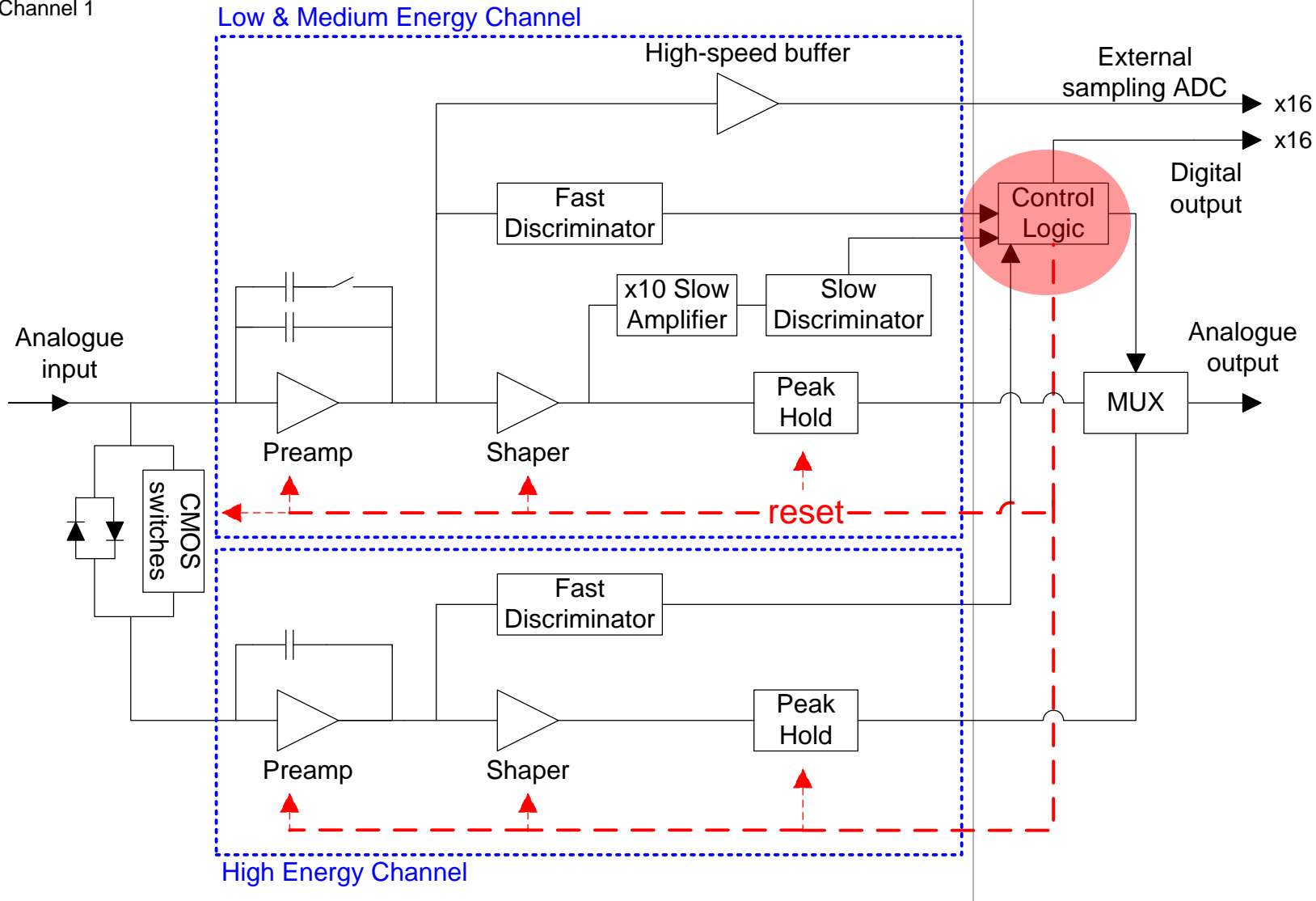






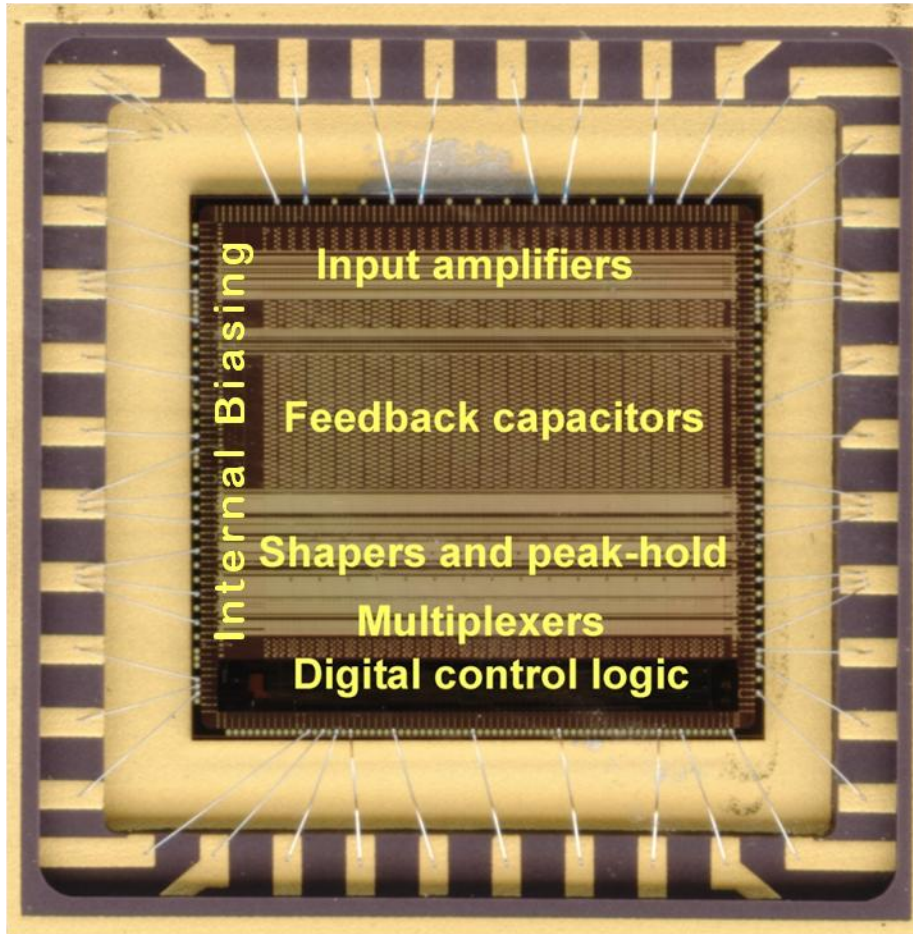






Layout

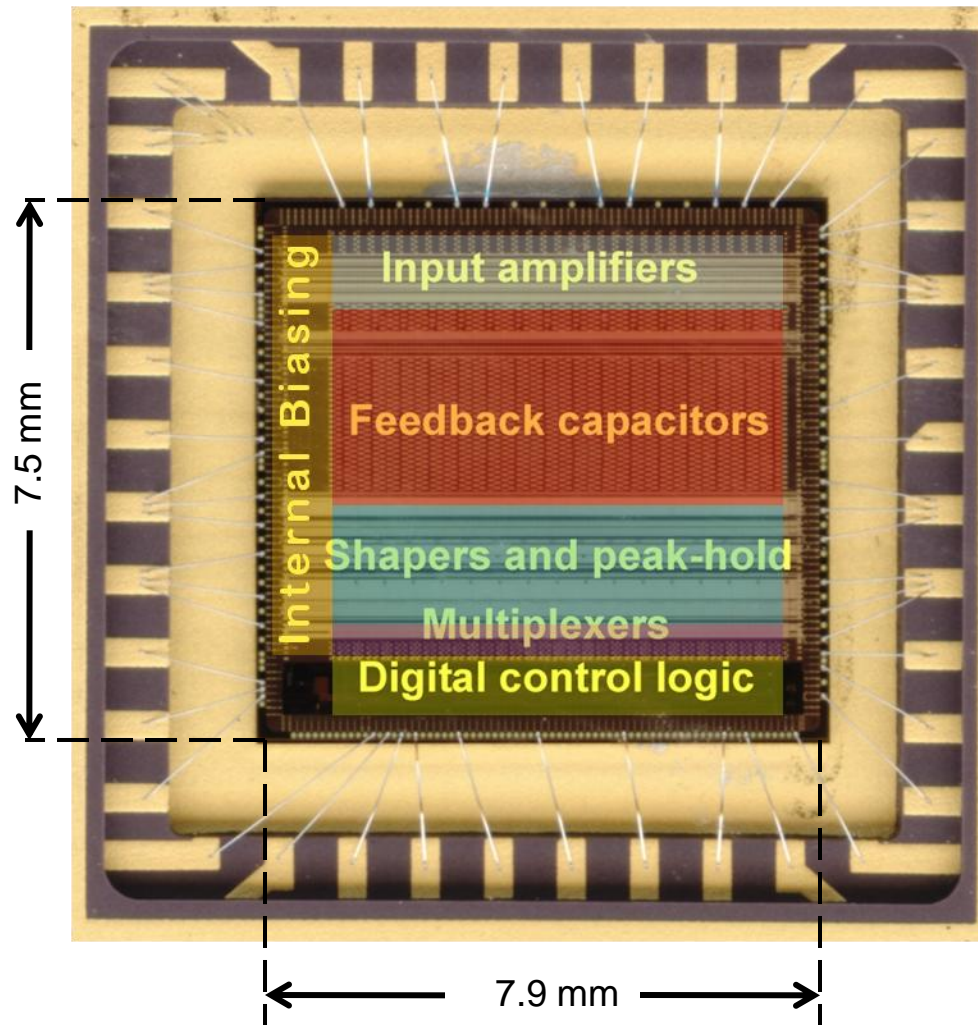
Front End



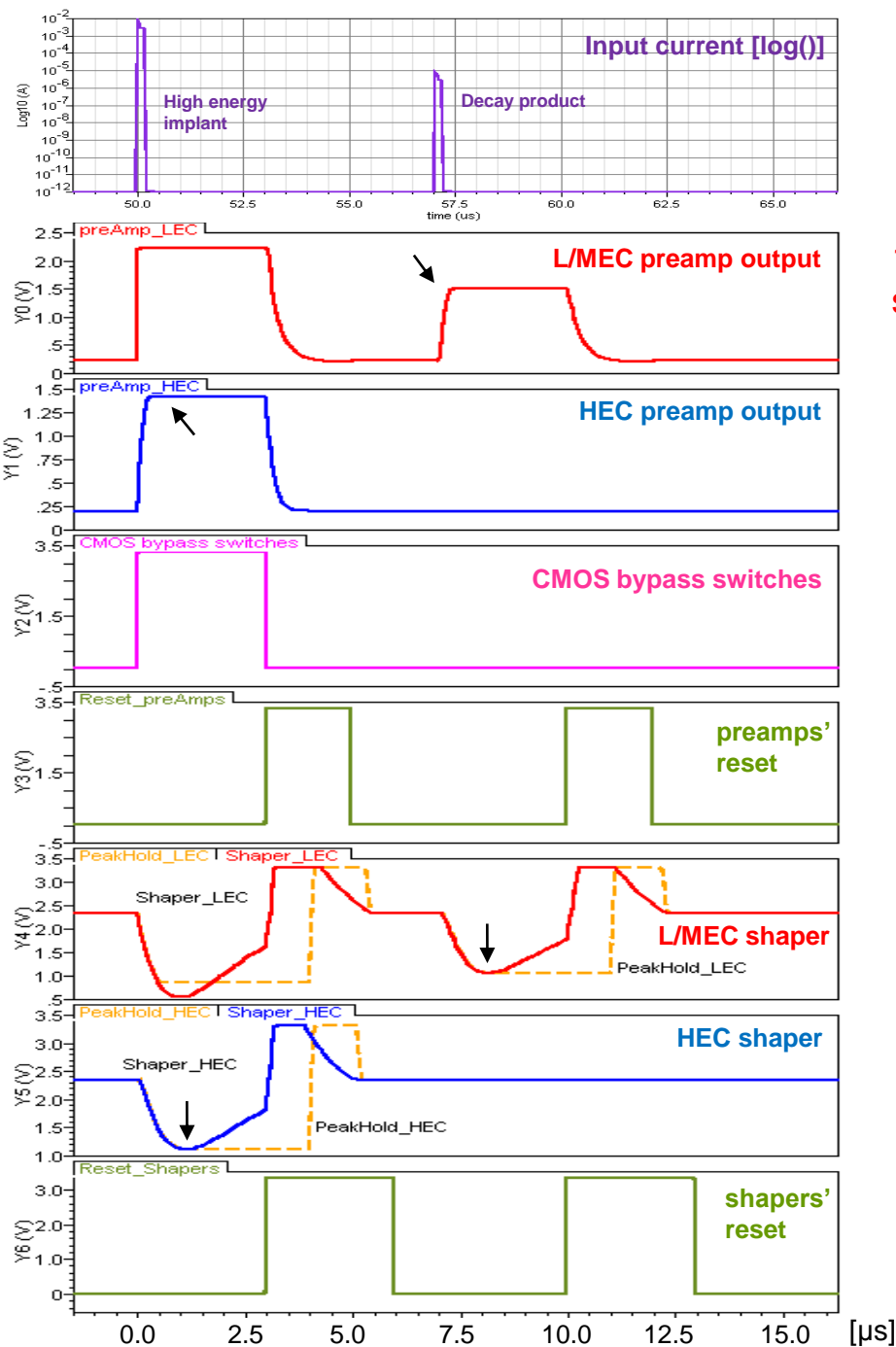
Back End

- Large feedback capacitors
- Internal biasing
- Low-impedance power distribution:
 - high number of PADs to gnd/vdd
 - maximized metal coverage

Layout



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- Internal biasing
- Low-impedance power distribution:
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 - maximized metal coverage



High-energy implant

1: LEC preamp saturates

3: HEC preamp integrates the input signal

2: CMOS bypass switches activate

5: preamplifiers (and CMOS switches) are reset

4: signal shaped, peak hold and read out

6: shapers are reset

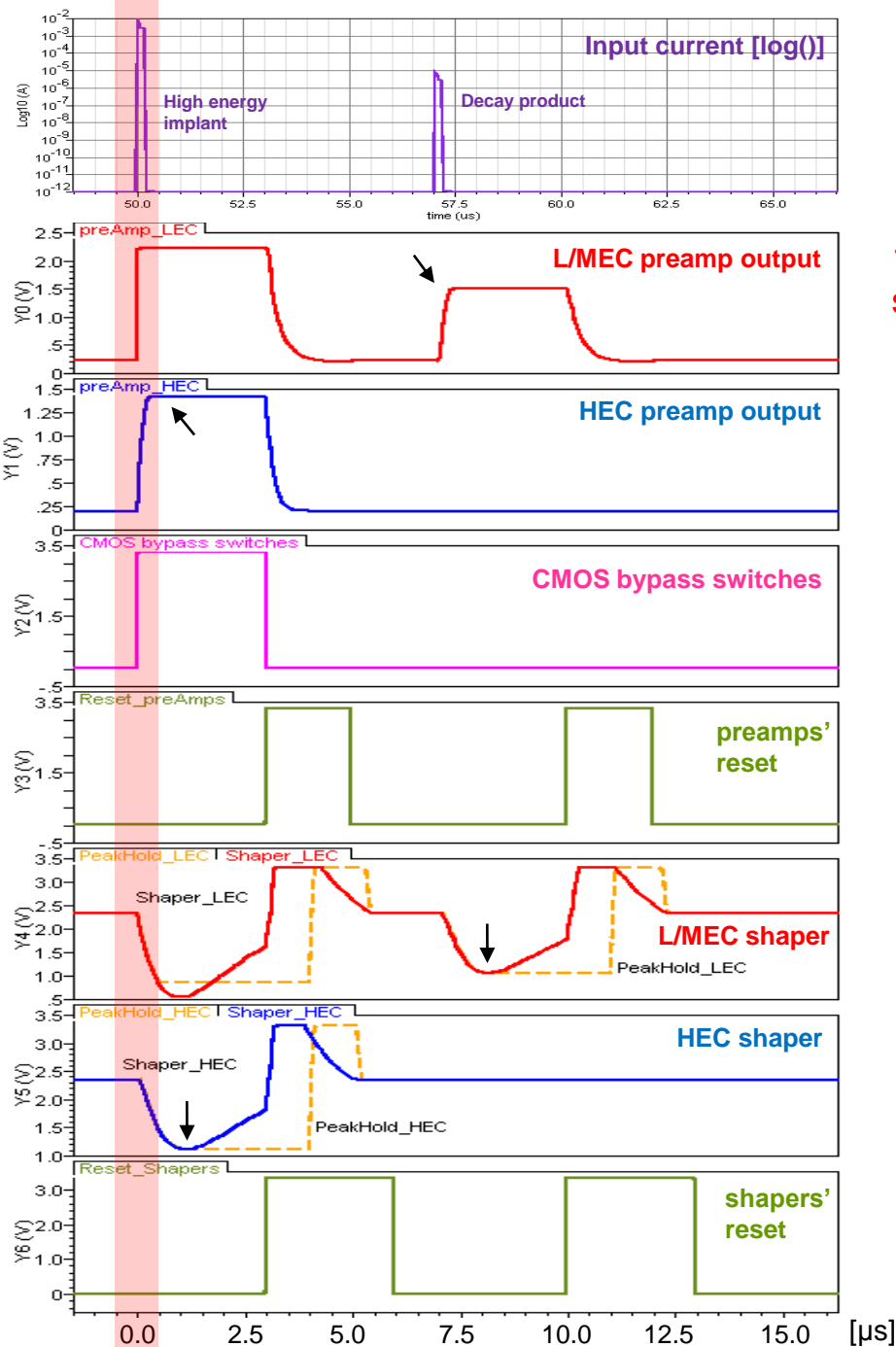
Low-energy decay event

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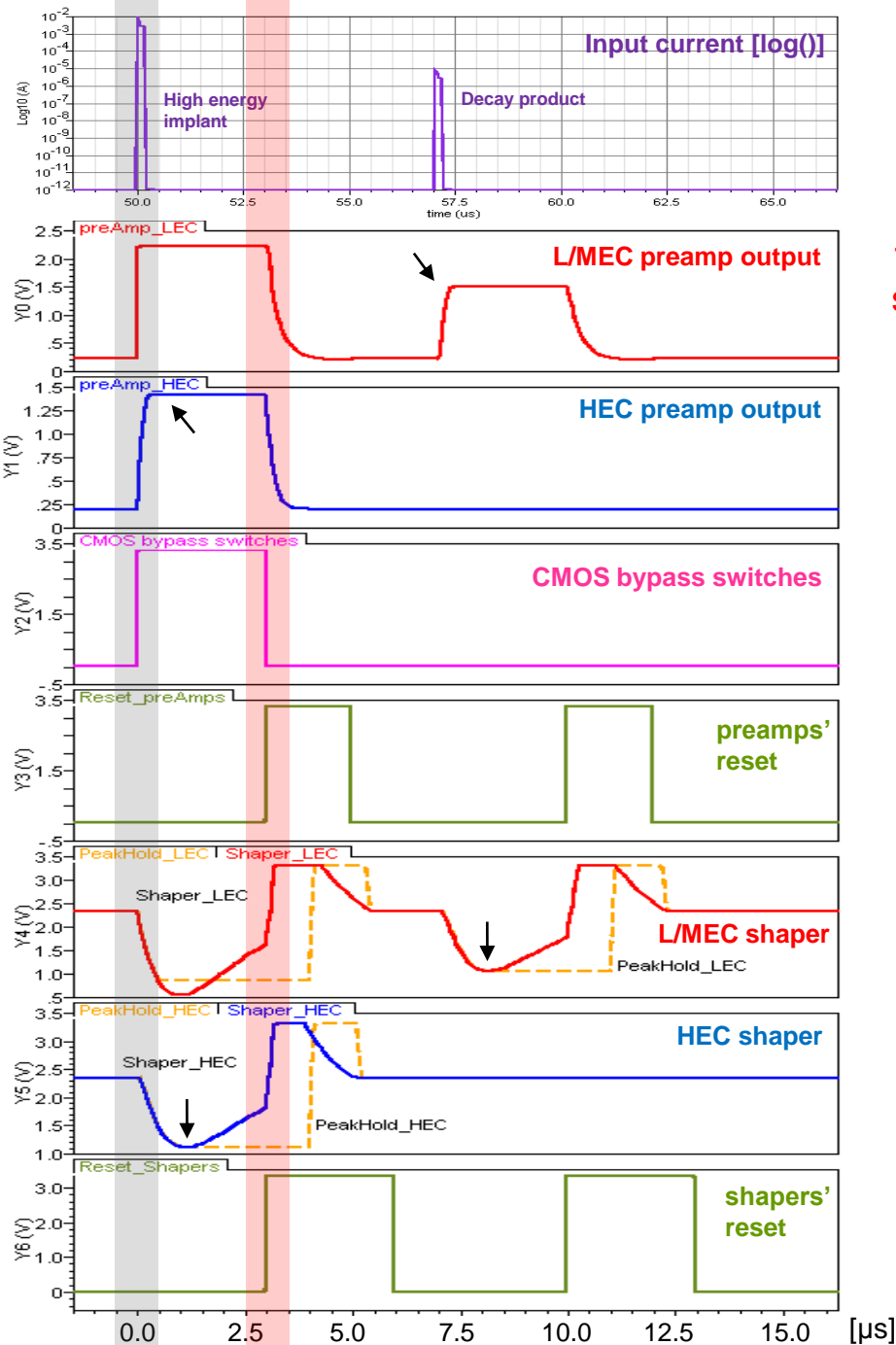
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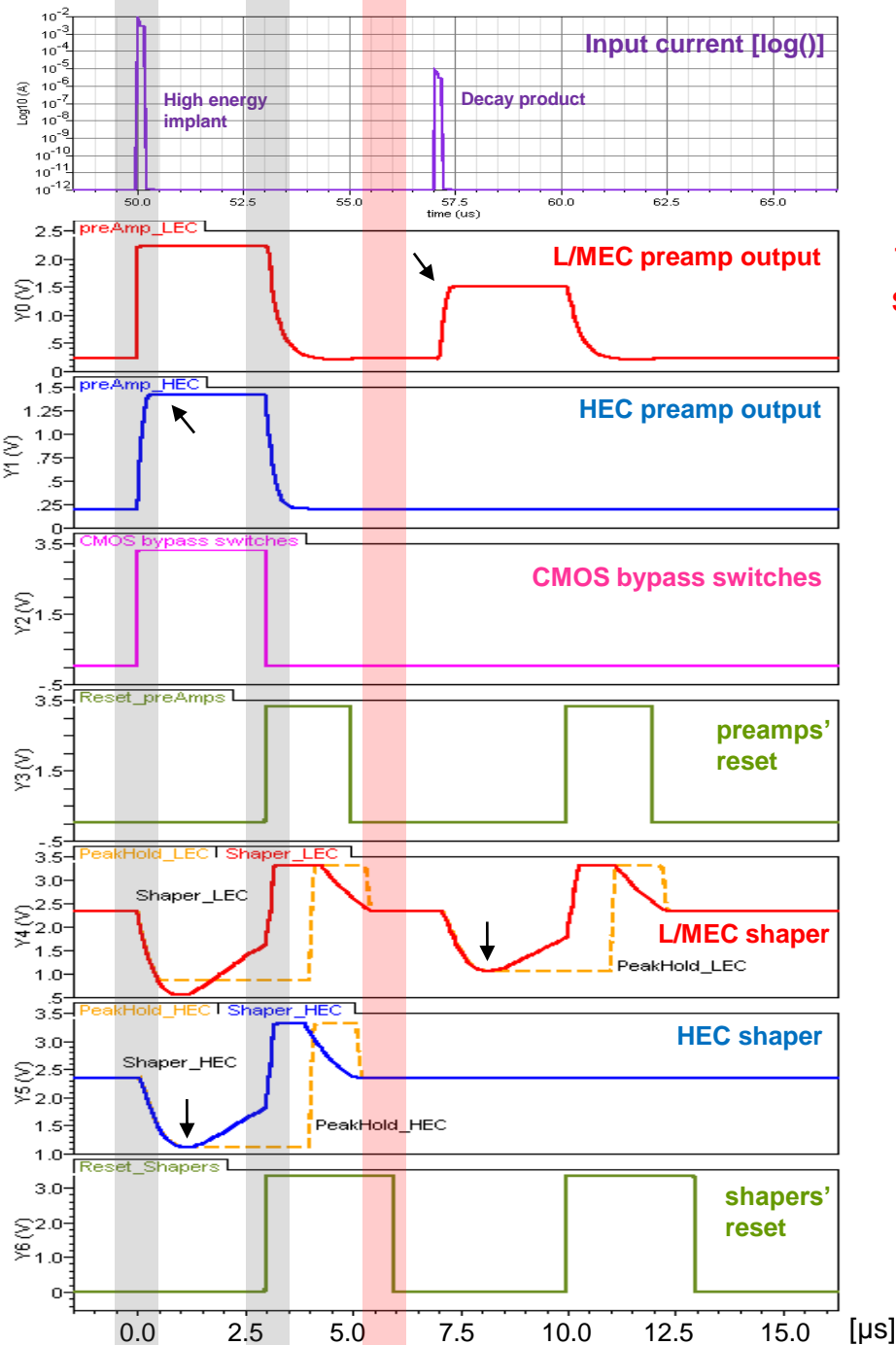
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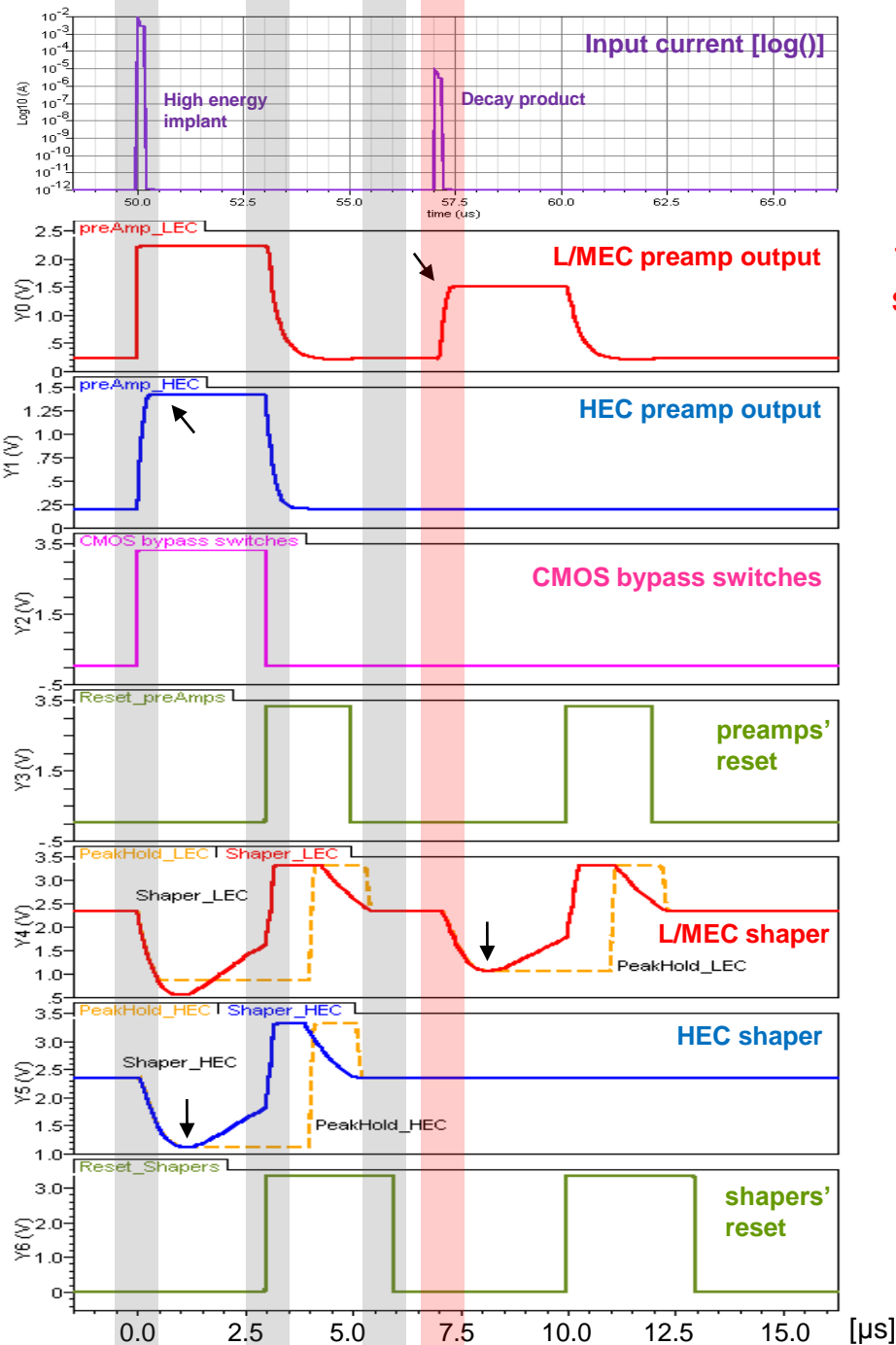
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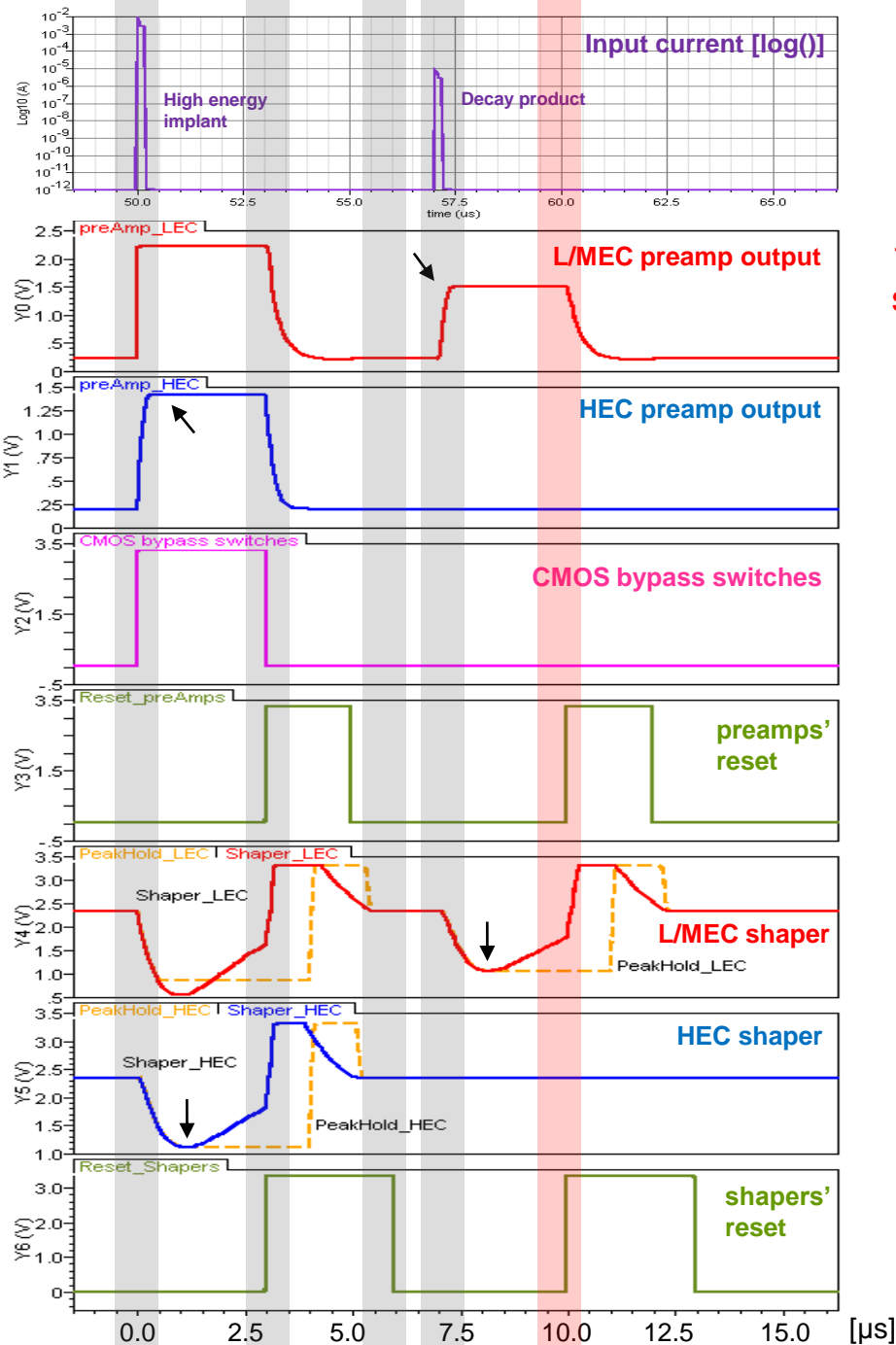
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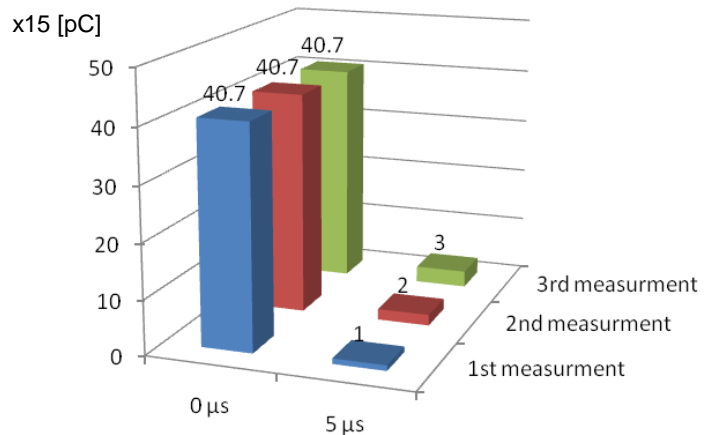
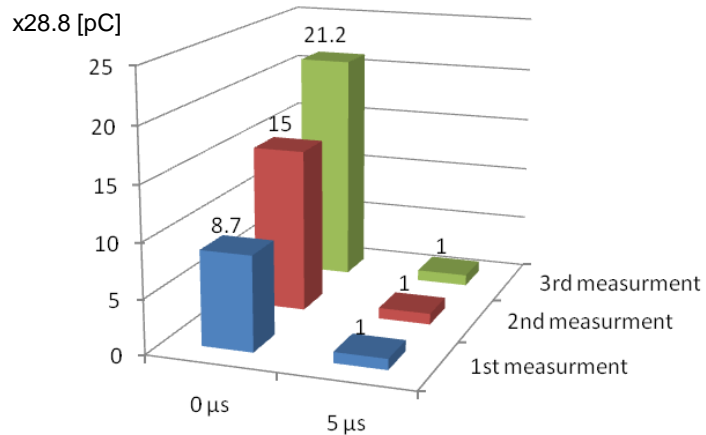
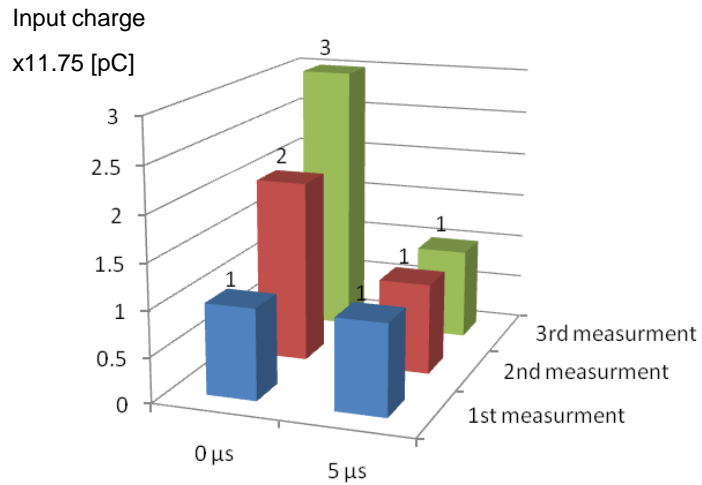
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Test sets

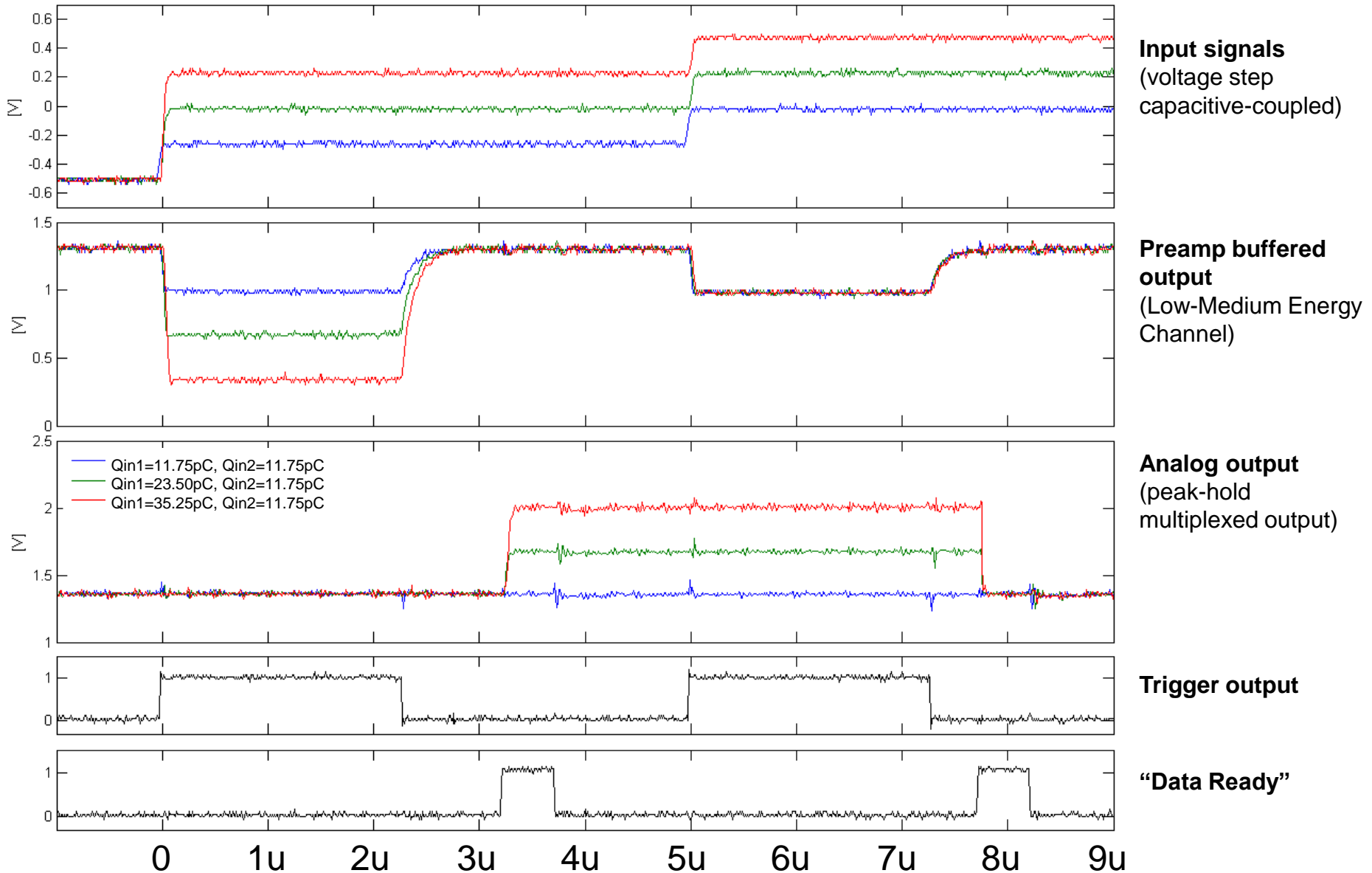
**1: variable Medium Energy (ME)
+
constant ME**

**2: variable High Energy (HE)
+
constant ME**

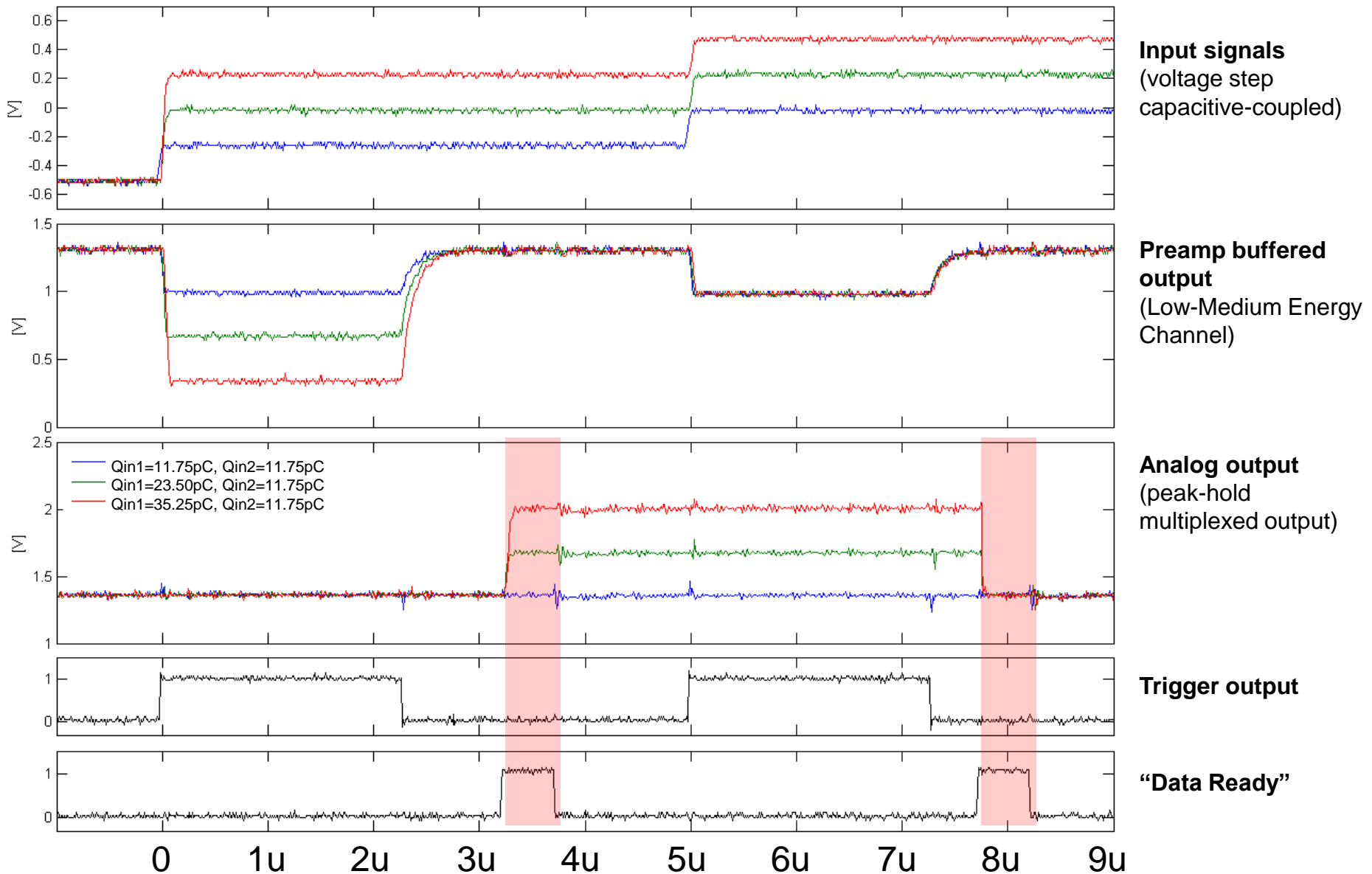
**3: constant High Energy (HE)
+
variable ME**



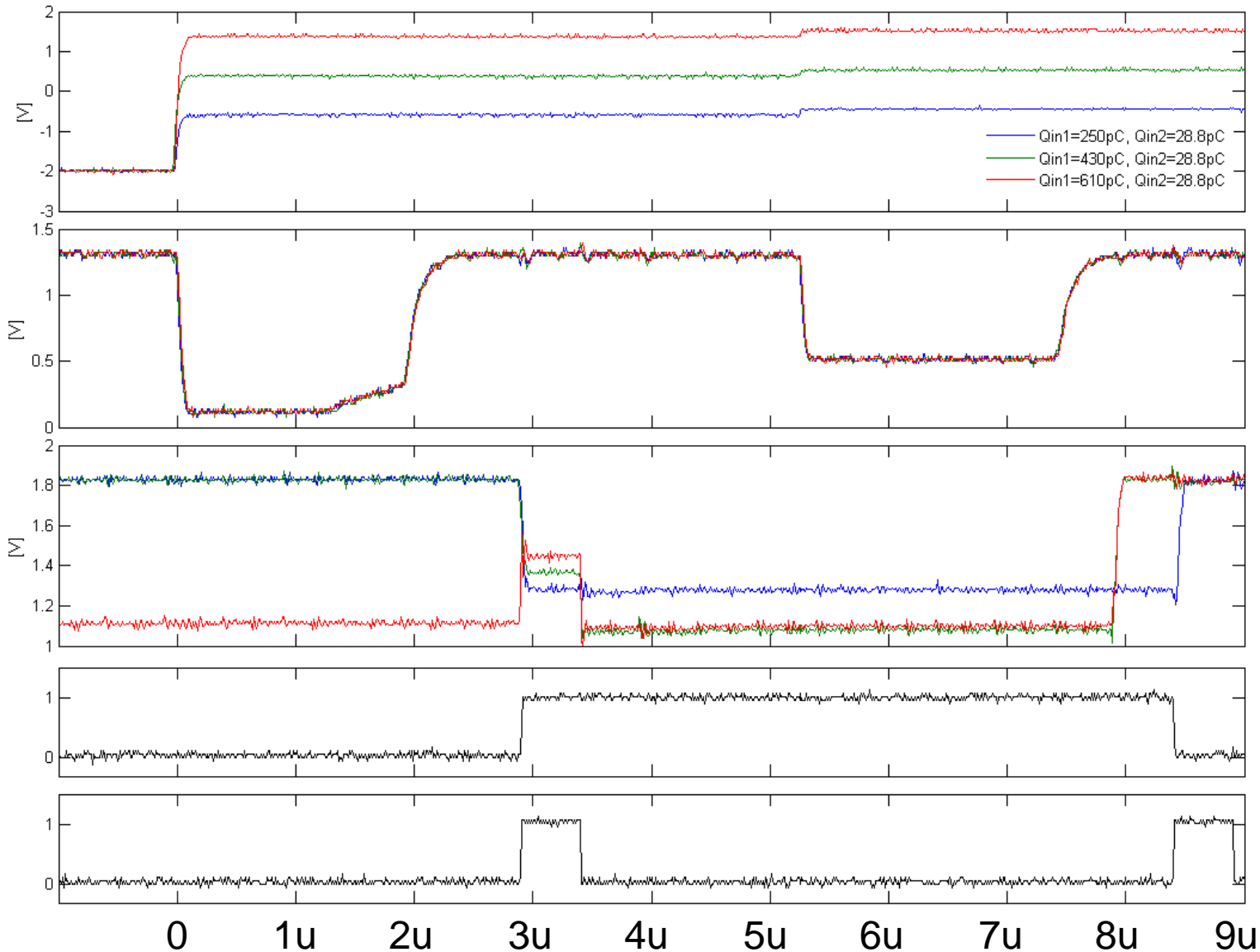
1: variable Medium Energy (ME) + constant ME



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2: variable High Energy (HE) + constant ME



Input signals
(voltage step
capacitive-coupled)

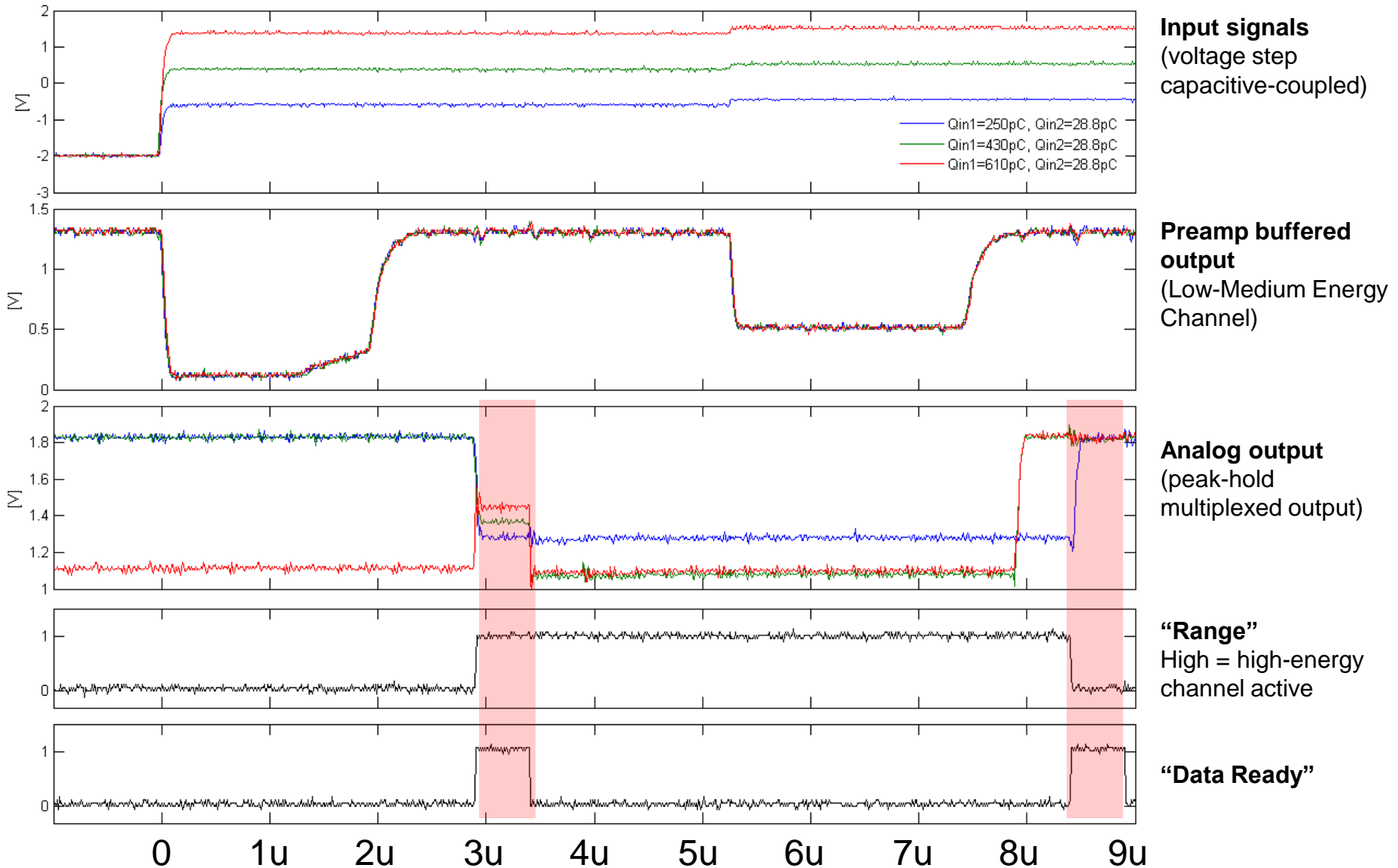
**Preamp buffered
output**
(Low-Medium Energy
Channel)

Analog output
(peak-hold
multiplexed output)

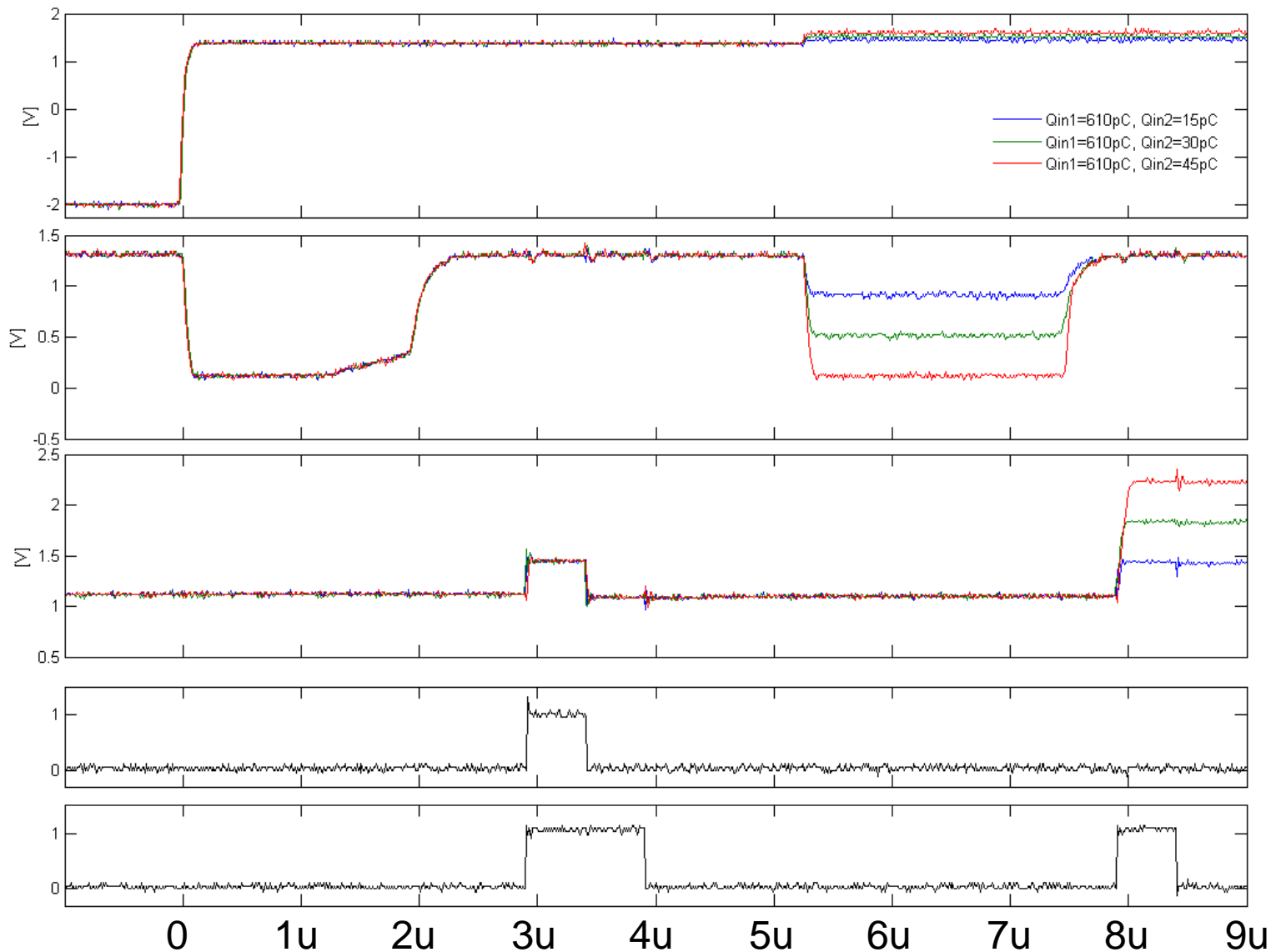
"Range"
High = high-energy
channel active

"Data Ready"

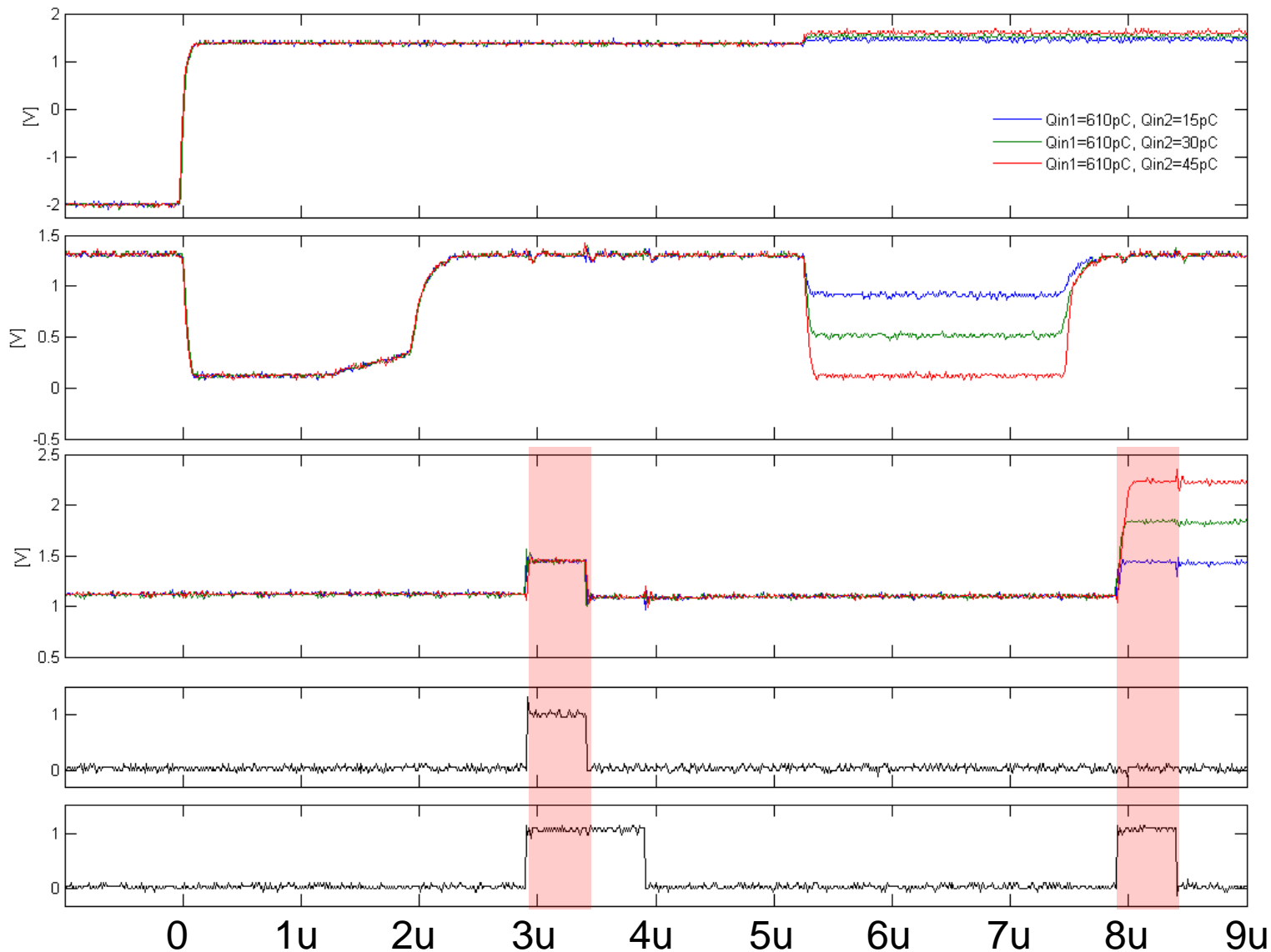
2: variable High Energy (HE) + constant ME



3: constant HE + variable ME



3: constant HE + variable ME



Input signals
(voltage step
capacitive-coupled)

**Preamp buffered
output**
(Low-Medium Energy
Channel)

Analog output
(peak-hold
multiplexed output)

"Range"
High = high-energy
channel active

"Data Ready"

Future development

- Support module, optimized for power supply distribution, shielding and decoupling, currently being manufactured;
- Once delivered, testing of analog performance and integration with data acquisition card (FPGA);
- Second and final iteration, with some minor adjustments and noise optimization, possibly on-chip calibration capacitors.

Acknowledgements

Thanks to:

- Thomas Davinson, *University of Edinburgh*
- Patrick Coleman-Smith, *STFC, Daresbury Laboratory*
- Ian Lazarus, *STFC, Daresbury Laboratory*