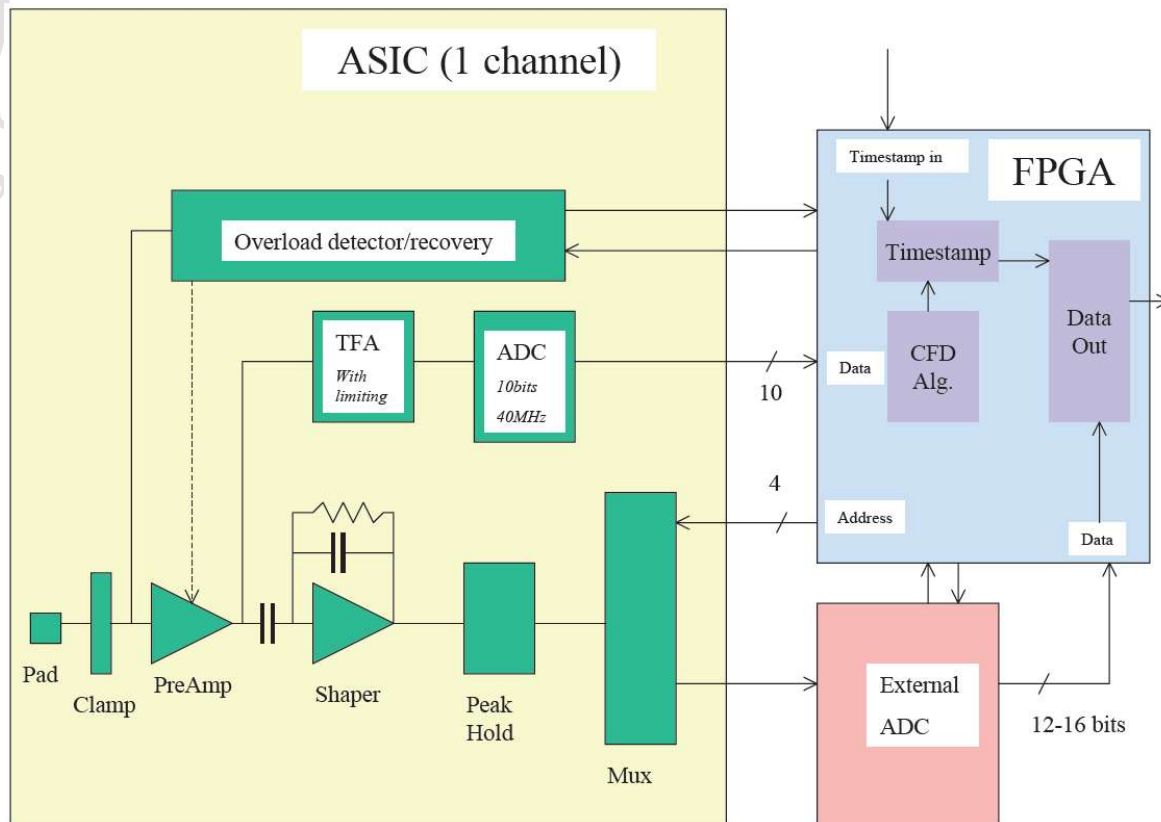# AIDA: ASIC *Concept*

- *Example* design concept
- 1 channel of 16 channel ASIC (shown with external FPGA and ADC)
- FEE-integrated DAQ
- Digital data via fibre-optic cable to PC-based data concentrator/event builder

courtesy I.Lazarus, CCLRC DL