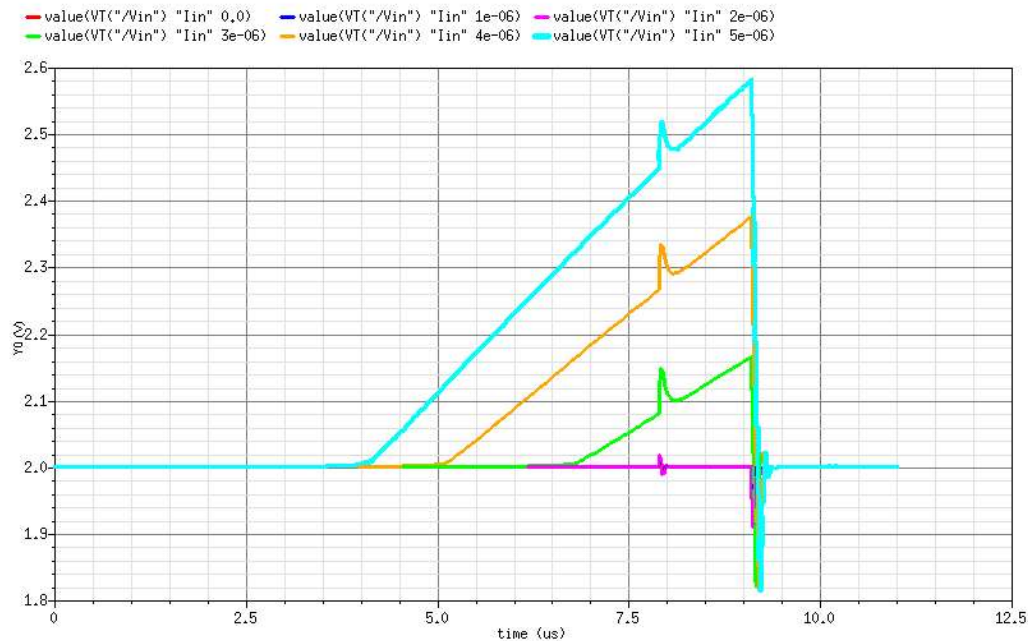


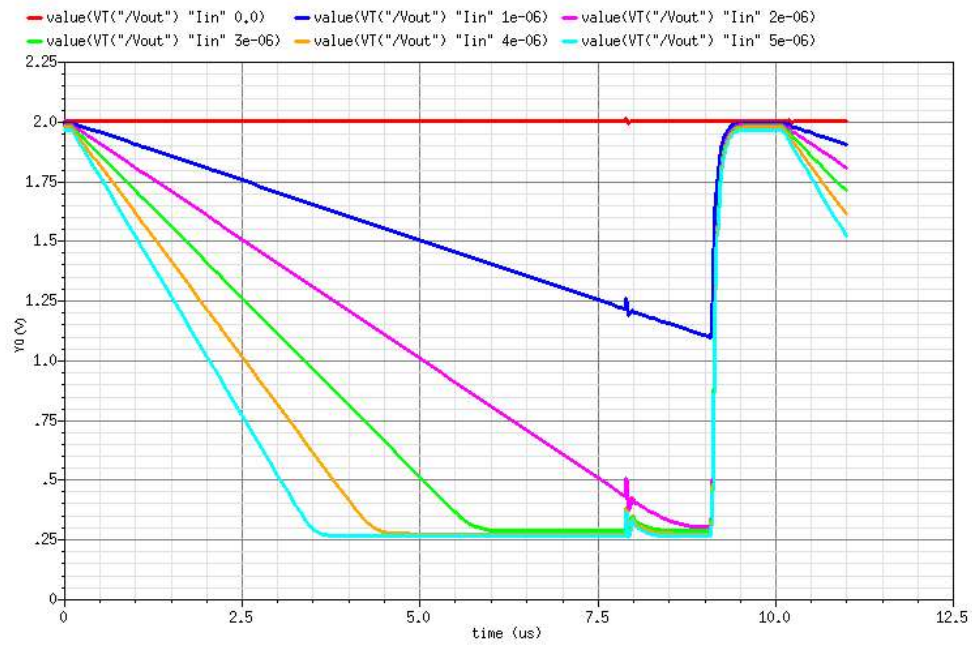
Constant current input:

Full-scale $1\text{ }\mu\text{A}$ for $10\text{ }\mu\text{s}$

0 to $5\text{ }\mu\text{A}$ by $1\text{ }\mu\text{A}$ steps

Clamp disabled



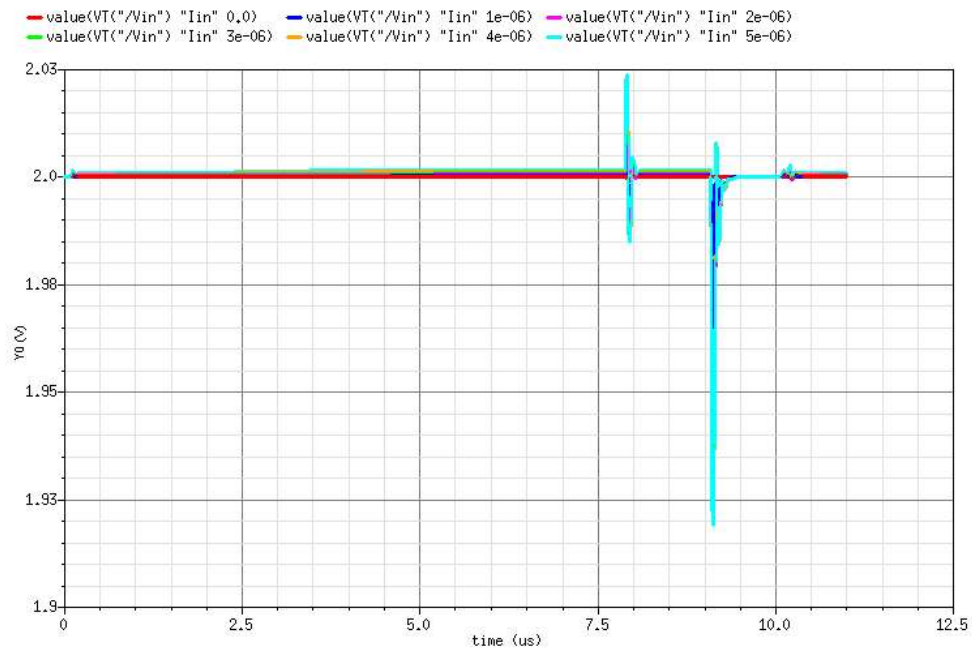


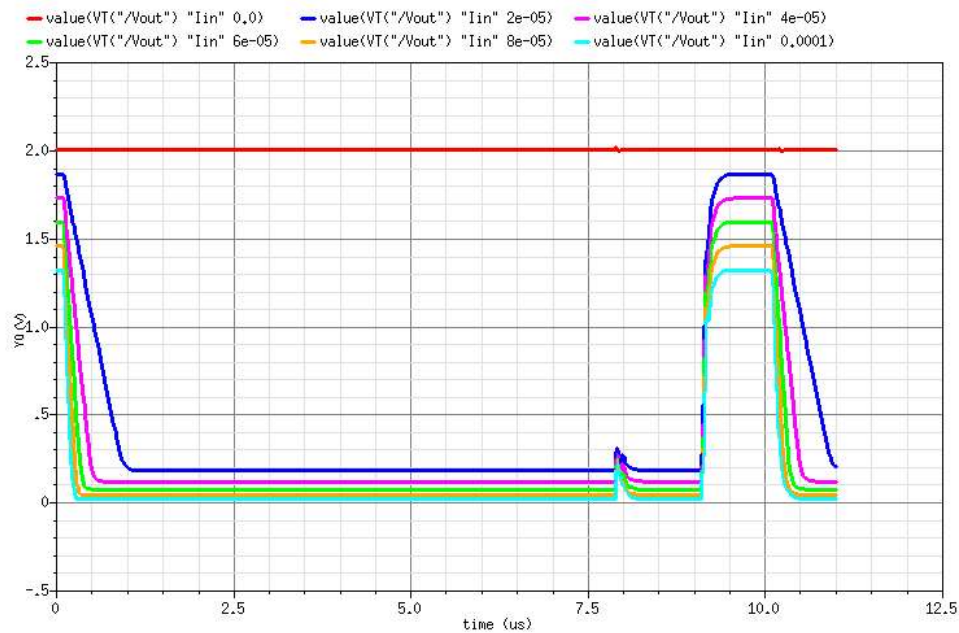
Constant current input:

Full-scale $1\mu\text{A}$ for $10\mu\text{s}$

0 to $5\mu\text{A}$ by $1\mu\text{A}$ steps

Clamp enabled



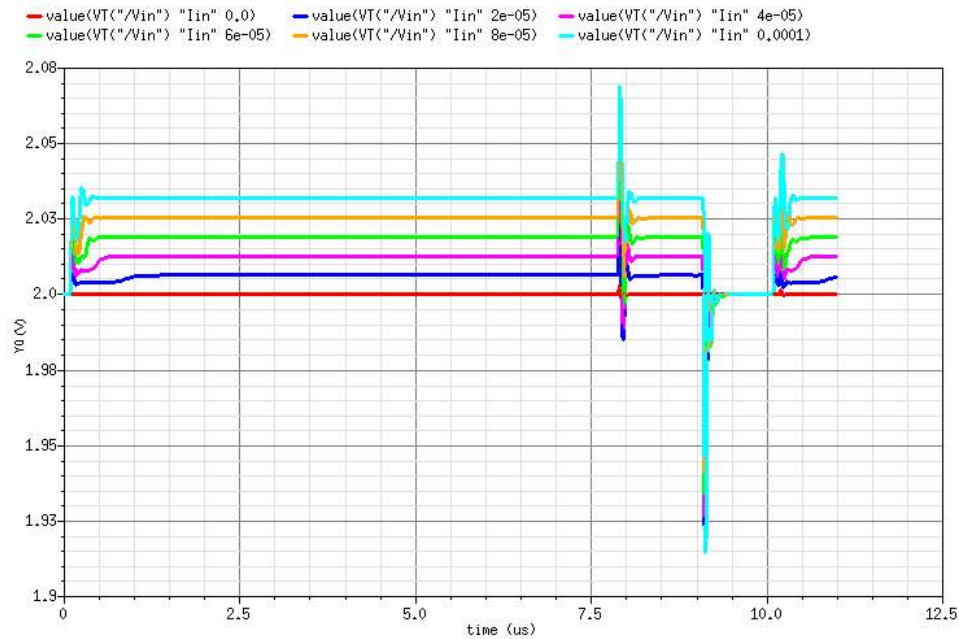


Constant current input:

Full-scale 1 μ A for 10 μ s

0 to 100 μ A by 20 μ A steps

Clamp enabled

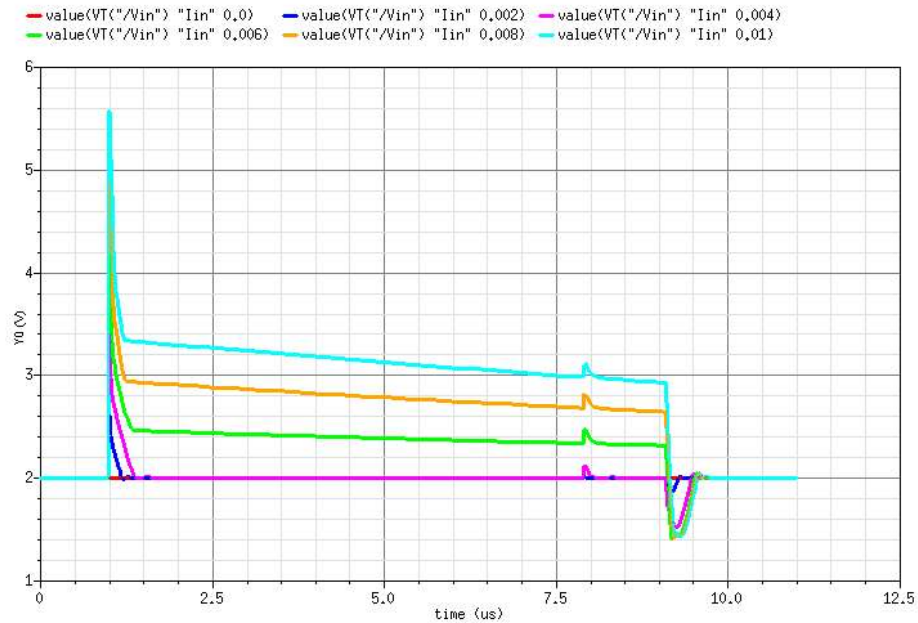
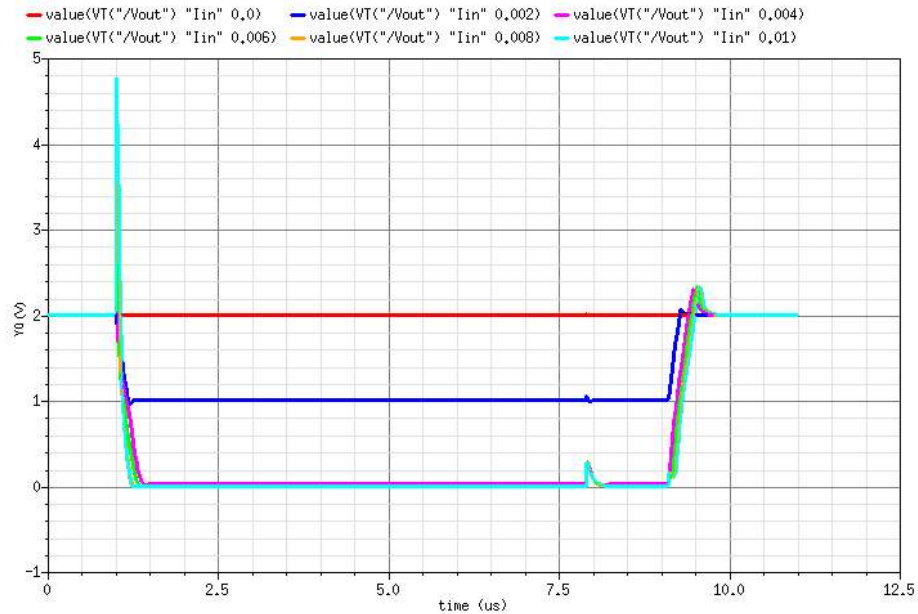


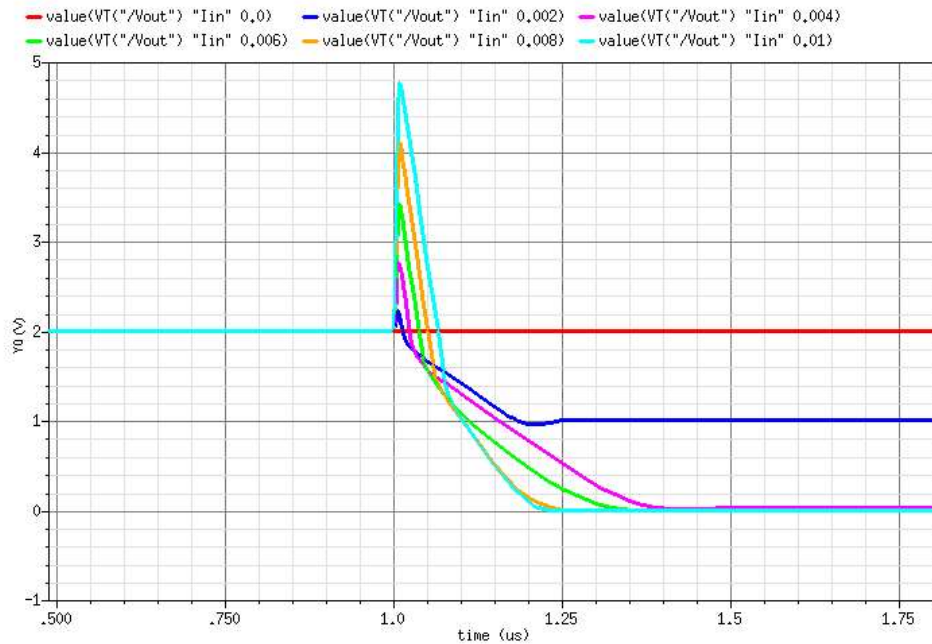
Pulse input:

Full-scale 2mA for 5ns

0 to 10mA by 2mA steps

Clamp disabled



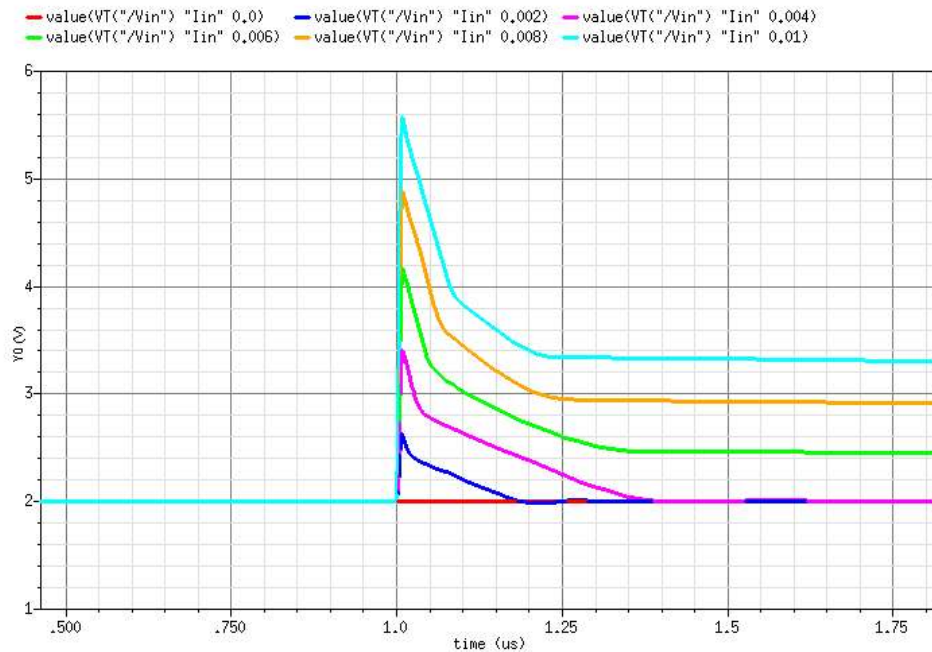


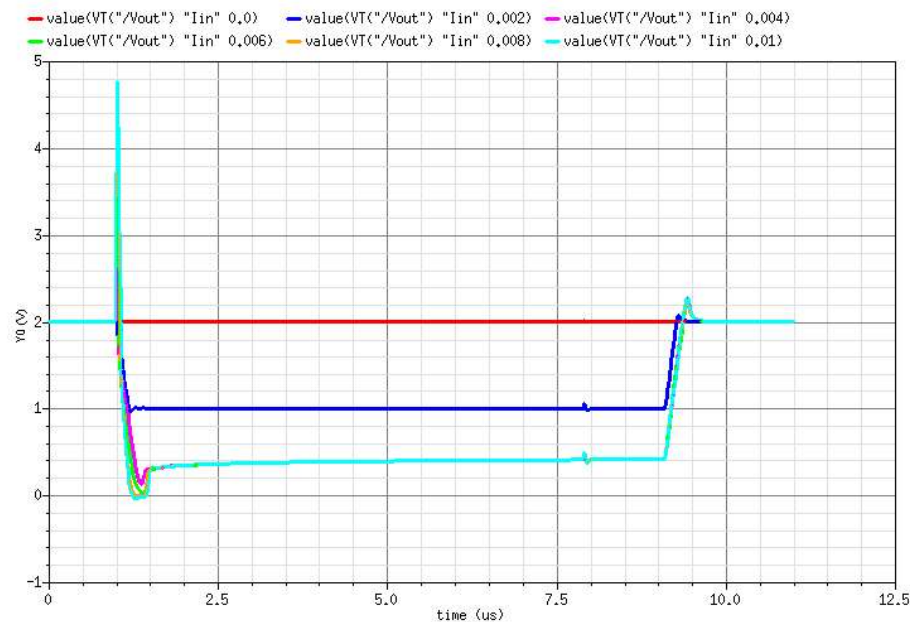
Pulse input:

Full-scale 2mA for 5ns

0 to 10mA by 2mA steps

Clamp disabled



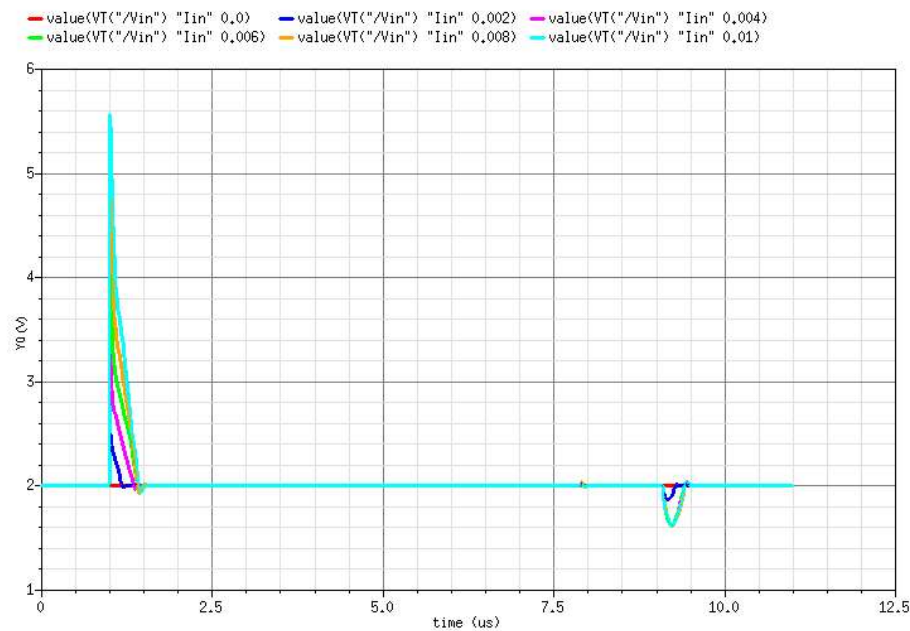


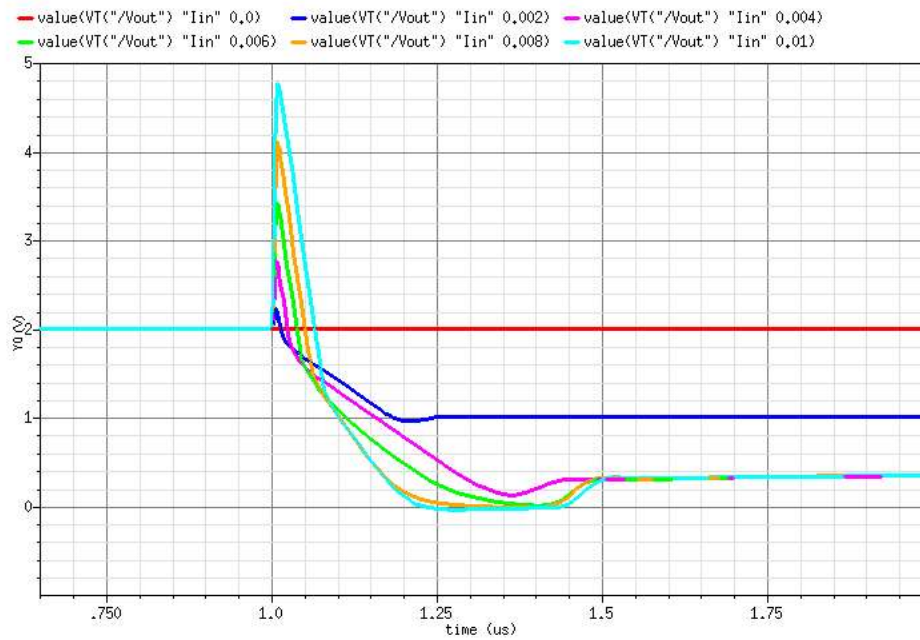
Pulse input:

Full-scale 2mA for 5ns

0 to 10mA by 2mA steps

Clamp enabled



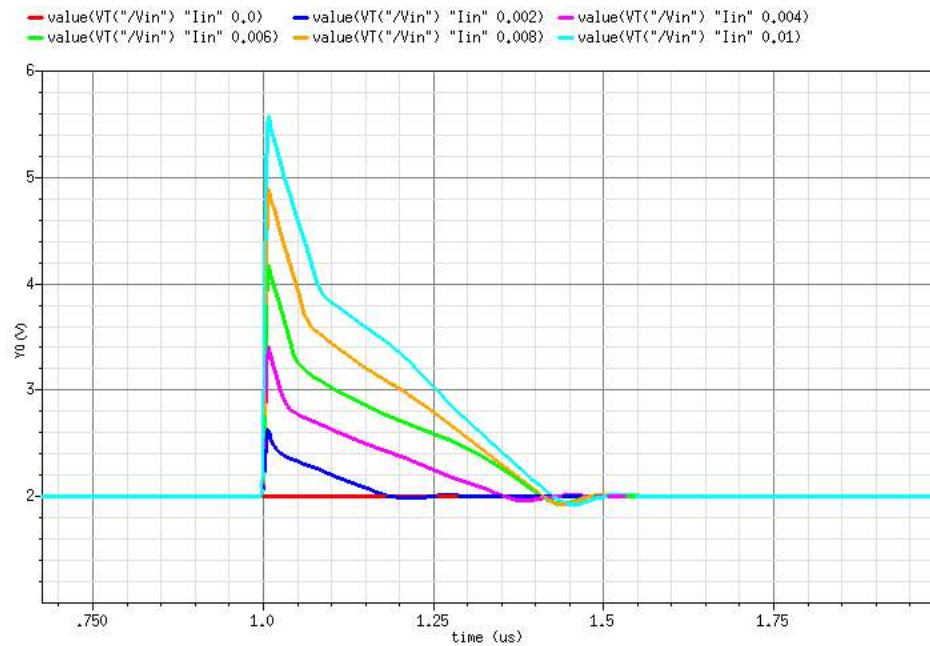


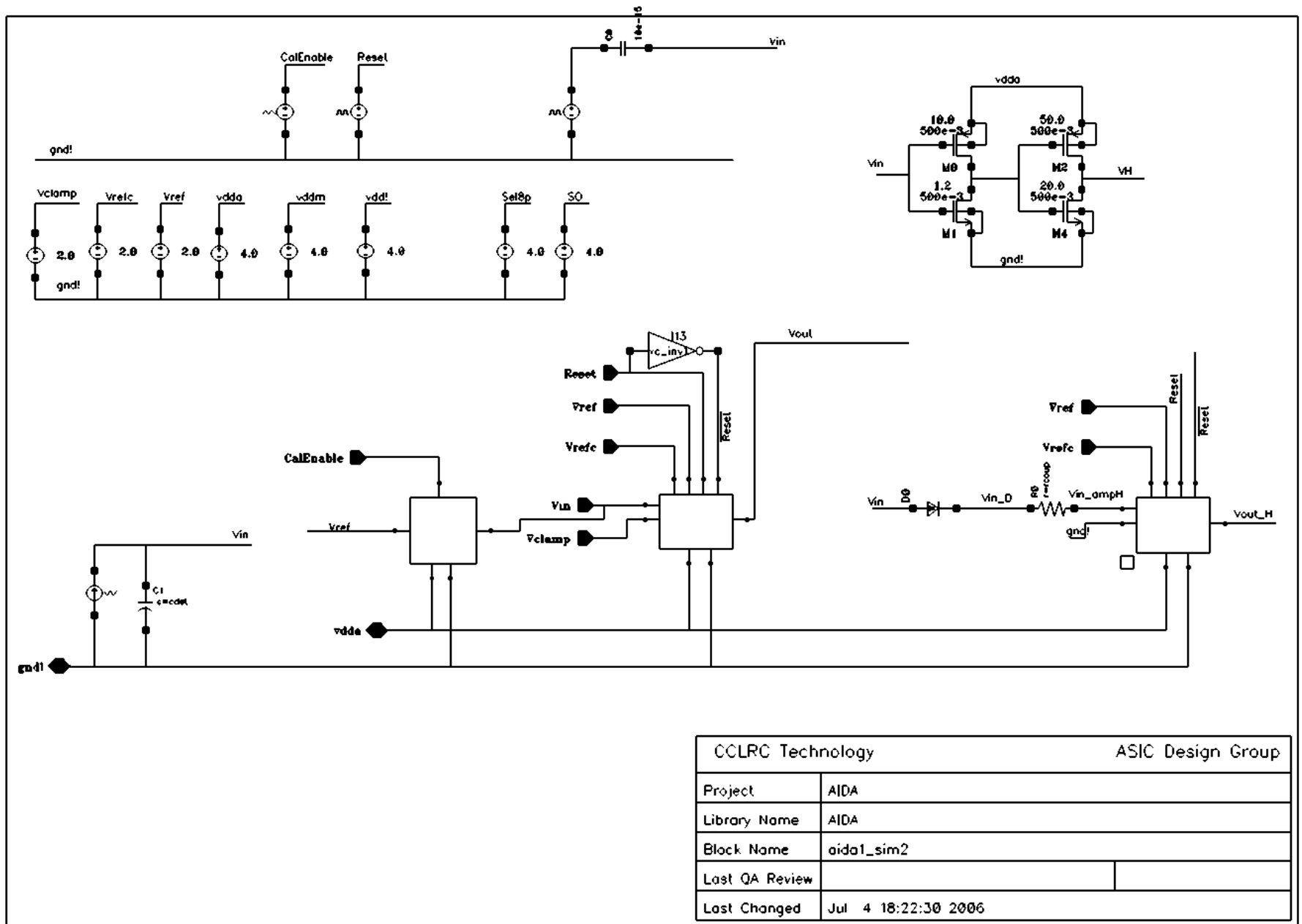
Pulse input:

Full-scale 2mA for 5ns

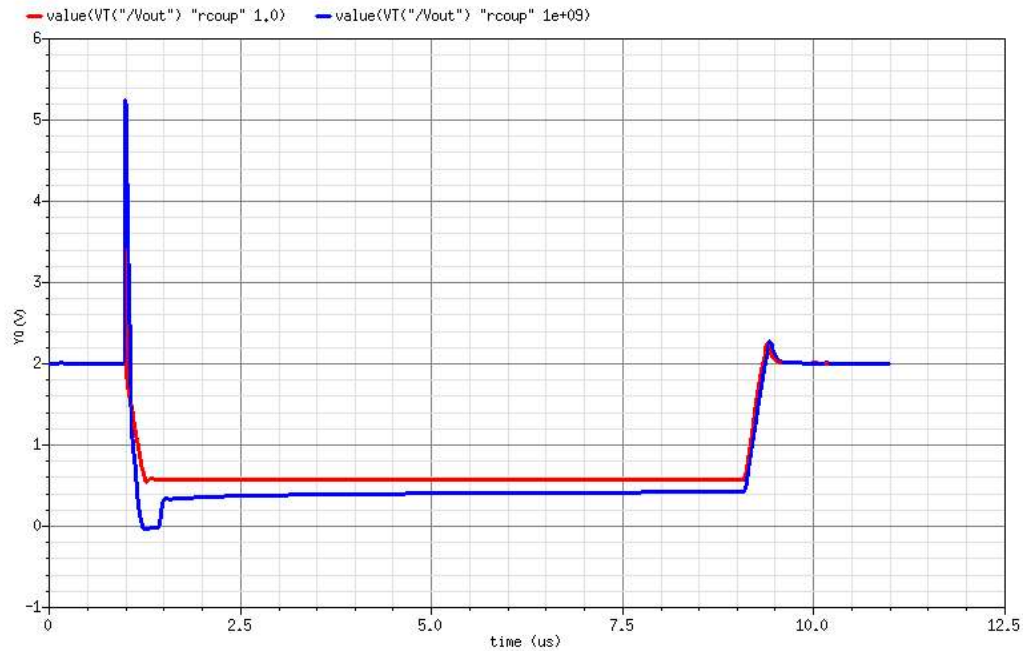
0 to 10mA by 2mA steps

Clamp enabled





CCLRC Technology		ASIC Design Group	
Project	AIDA		
Library Name	AIDA		
Block Name	aida1_sim2		
Last QA Review			
Last Changed	Jul 4 18:22:30 2006		

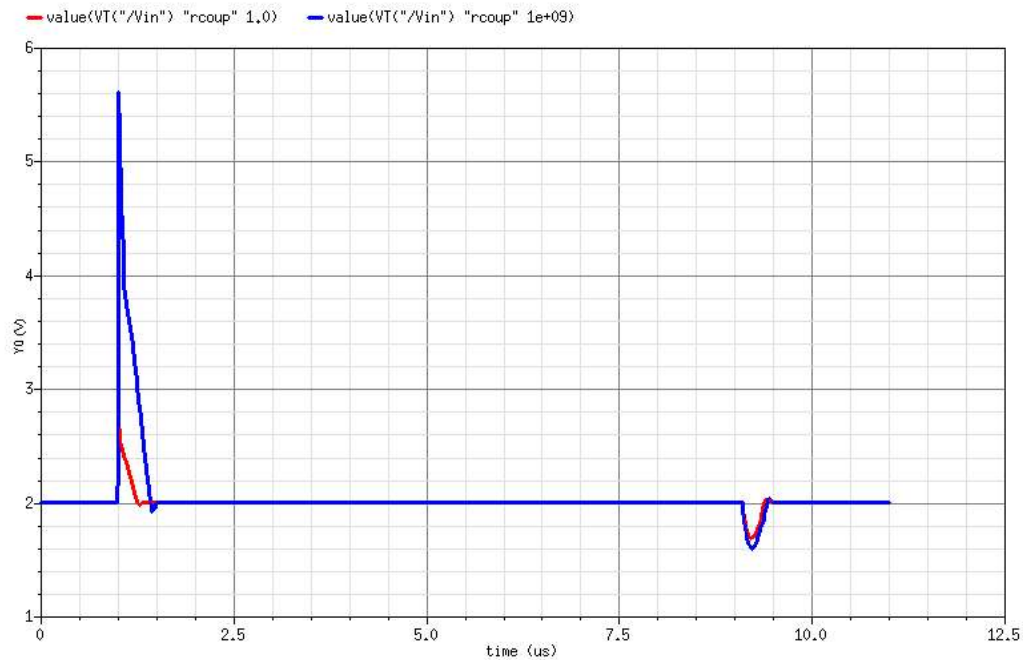


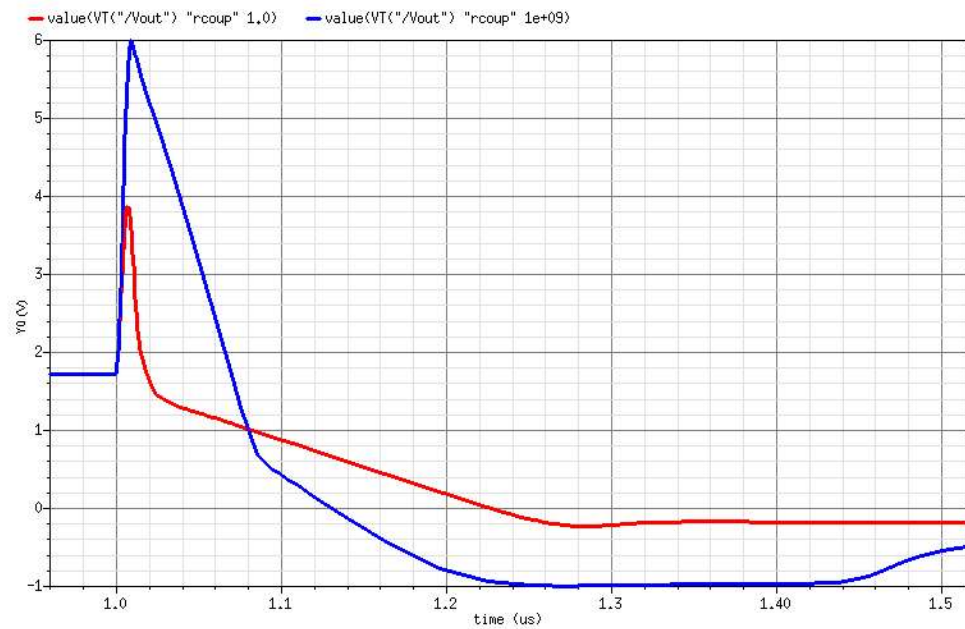
Pulse input 10mA:

Full-scale 2mA for 5ns

Coupling resistor On/Off

Clamp enabled



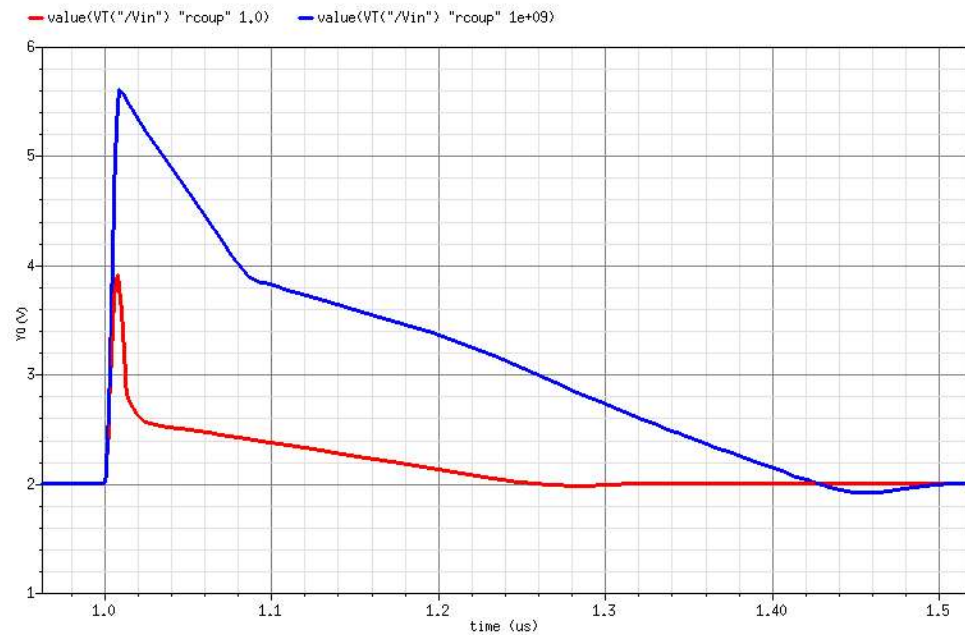


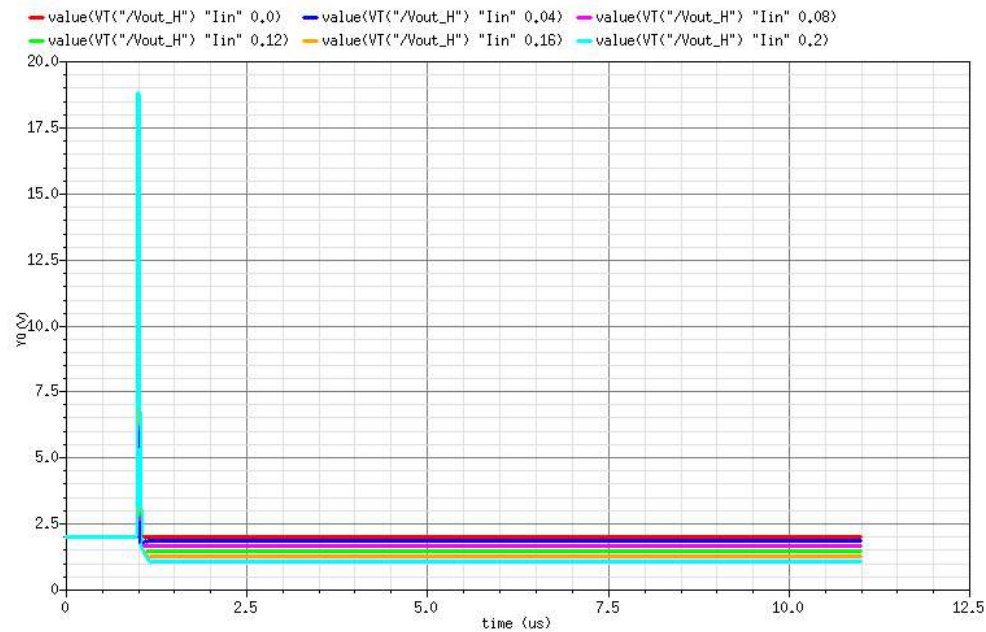
Pulse input 10mA:

Full-scale 2mA for 5ns

Coupling resistor On/Off

Clamp enabled





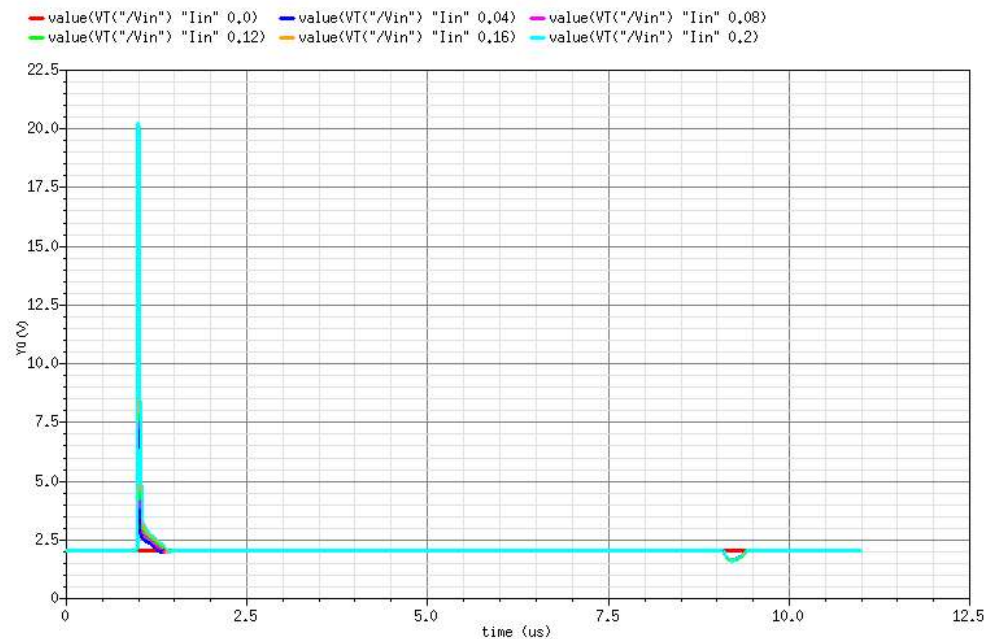
Pulse input:

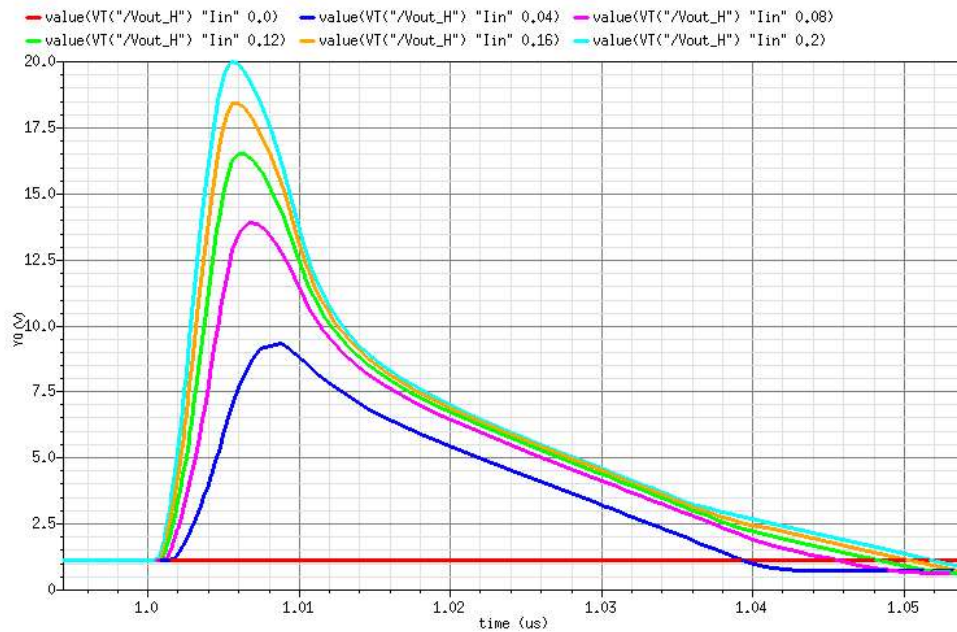
Full-scale 2mA for 5ns

Coupling resistor On

0-200mA for 5ns

Clamp enabled





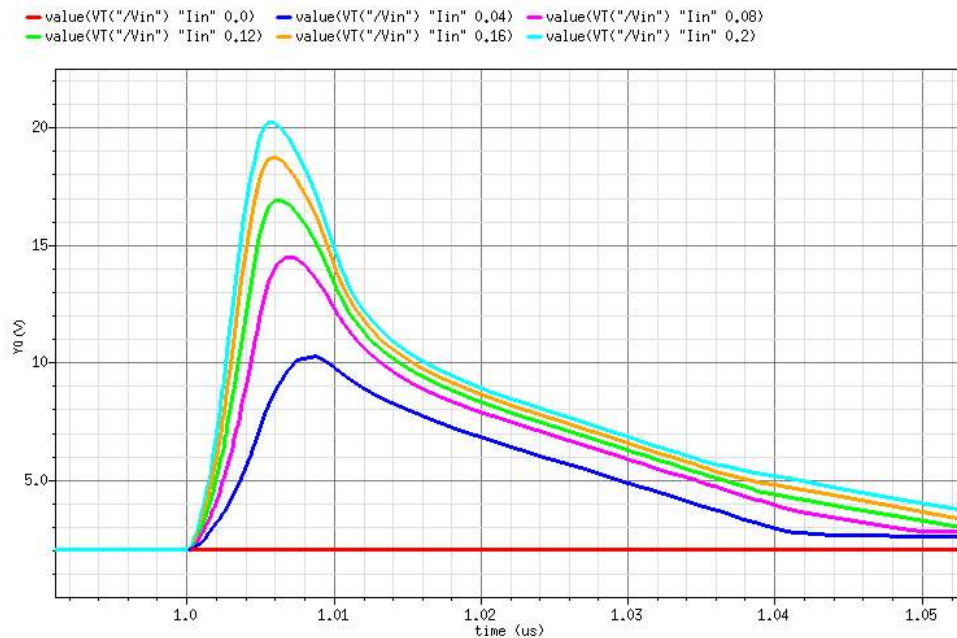
Pulse input:

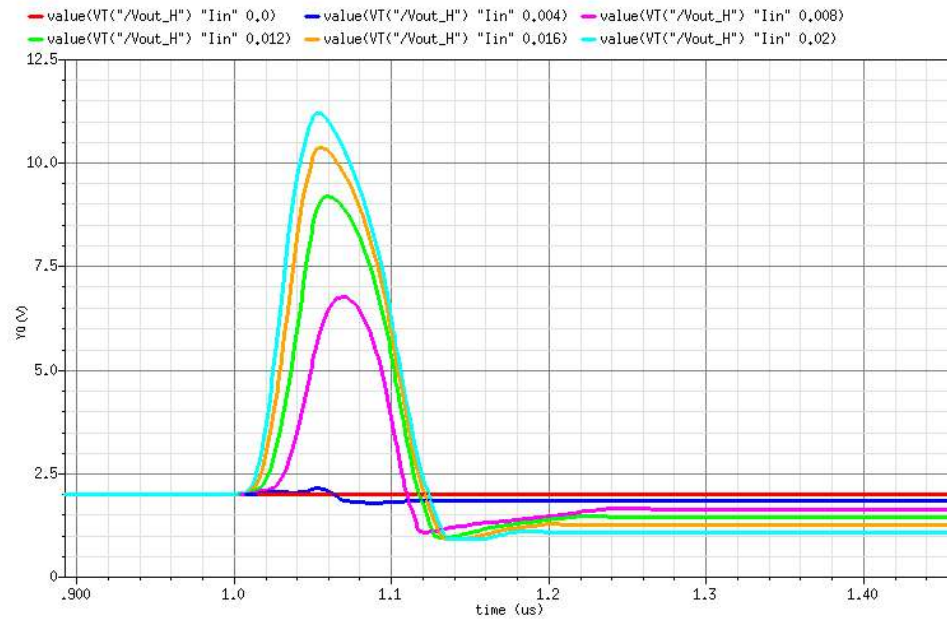
Full-scale 2mA for 5ns

Coupling resistor On

0-200mA for 5ns

Clamp enabled





Pulse input:

Full-scale 0.2mA for 50ns

Coupling resistor On

0-20mA for 50ns

Clamp enabled

