

A

B

C

D

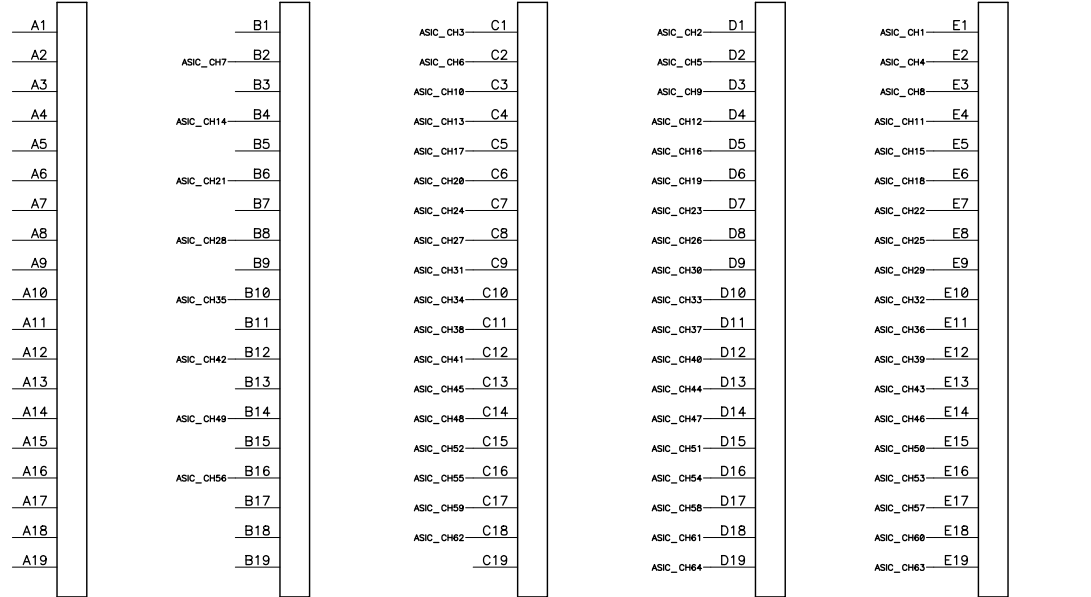
P1:A

P1:B

P1:C

P1:D

P1:E



ERNI95PIN-SOCKET-RA

ERNI95PIN-SOCKET-RA

ERNI95PIN-SOCKET-RA

ERNI95PIN-SOCKET-RA

ERNI95PIN-SOCKET-RA

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A

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A

B

C

D

CN2	
GND	1
ASIC4_AOUT	2
GND	3
ASIC4_DISOR15	4
ASIC4_DISOR13	5
ASIC4_DISOR11	6
ASIC4_DISOR9	7
ASIC4_DISOR7	8
ASIC4_DISOR5	9
ASIC4_DISOR3	10
ASIC4_DISOR1	11
GND	12
ASIC4_RDO_RANGE	13
ASIC4_RDO_A2	14
ASIC4_RDO_A8	15
ASIC4_DATA_READY	16
ASIC4_SHIFT_OUT	17
GND	18
ASIC3_AOUT	19
GND	20
ASIC3_DISOR15	21
ASIC3_DISOR13	22
ASIC3_DISOR11	23
ASIC3_DISOR9	24
ASIC3_DISOR7	25
ASIC3_DISOR5	26
ASIC3_DISOR3	27
ASIC3_DISOR1	28
GND	29
ASIC3_RDO_RANGE	30
ASIC3_RDO_A2	31
ASIC3_RDO_A8	32
ASIC3_DATA_READY	33
ASIC3_SHIFT_OUT	34
GND	35

SAMTEC_FSI_70_MATING

CN1	
GND	1
ASIC2_AOUT	2
GND	3
ASIC2_DISOR15	4
ASIC2_DISOR13	5
ASIC2_DISOR11	6
ASIC2_DISOR9	7
ASIC2_DISOR7	8
ASIC2_DISOR5	9
ASIC2_DISOR3	10
ASIC2_DISOR1	11
GND	12
ASIC2_RDO_RANGE	13
ASIC2_RDO_A2	14
ASIC2_RDO_A8	15
ASIC2_DATA_READY	16
ASIC2_SHIFT_OUT	17
GND	18
ASIC1_AOUT	19
GND	20
ASIC1_DISOR15	21
ASIC1_DISOR13	22
ASIC1_DISOR11	23
ASIC1_DISOR9	24
ASIC1_DISOR7	25
ASIC1_DISOR5	26
ASIC1_DISOR3	27
ASIC1_DISOR1	28
GND	29
ASIC1_RDO_RANGE	30
ASIC1_RDO_A2	31
ASIC1_RDO_A8	32
ASIC1_DATA_READY	33
ASIC1_SHIFT_OUT	34
GND	35

SAMTEC_FSI_70_MATING

CN4	
ASIC4_SH_VREF	1
ASIC4_VREF	2
ASIC4_AN_BF15	3
GND	4
ASIC4_AN_BF13	5
GND	6
ASIC4_AN_BF11	7
GND	8
ASIC4_AN_BF9	9
GND	10
ASIC4_AN_BF7	11
GND	12
ASIC4_AN_BF5	13
GND	14
ASIC4_AN_BF3	15
GND	16
ASIC4_AN_BF1	17
ASIC3_VREF	18
ASIC3_AN_BF16	19
GND	20
ASIC3_AN_BF14	21
GND	22
ASIC3_AN_BF12	23
GND	24
ASIC3_AN_BF10	25
GND	26
ASIC3_AN_BF8	27
GND	28
ASIC3_AN_BF6	29
GND	30
ASIC3_AN_BF4	31
GND	32
ASIC3_AN_BF2	33
GND	34
ASIC3_SH_VREF	35

SAMTEC_FSI_70_MATING

CN3	
ASIC2_SH_VREF	1
ASIC2_VREF	2
ASIC2_AN_BF15	3
GND	4
ASIC2_AN_BF13	5
GND	6
ASIC2_AN_BF11	7
GND	8
ASIC2_AN_BF9	9
GND	10
ASIC2_AN_BF7	11
GND	12
ASIC2_AN_BF5	13
GND	14
ASIC2_AN_BF3	15
GND	16
ASIC2_AN_BF1	17
GND	18
PWR_MEZZ_3V3_3A	19
ASIC1_VREF	20
ASIC1_AN_BF14	21
GND	22
ASIC1_AN_BF12	23
GND	24
ASIC1_AN_BF10	25
GND	26
ASIC1_AN_BF8	27
GND	28
ASIC1_AN_BF6	29
GND	30
ASIC1_AN_BF4	31
GND	32
ASIC1_AN_BF2	33
GND	34
ASIC1_SH_VREF	35
ASIC_TEMP_CS8	36

SAMTEC_FSI_70_MATING

2 1 3.3V_DRIVER
 FB2E_PANASONICFB22
 220R_2A

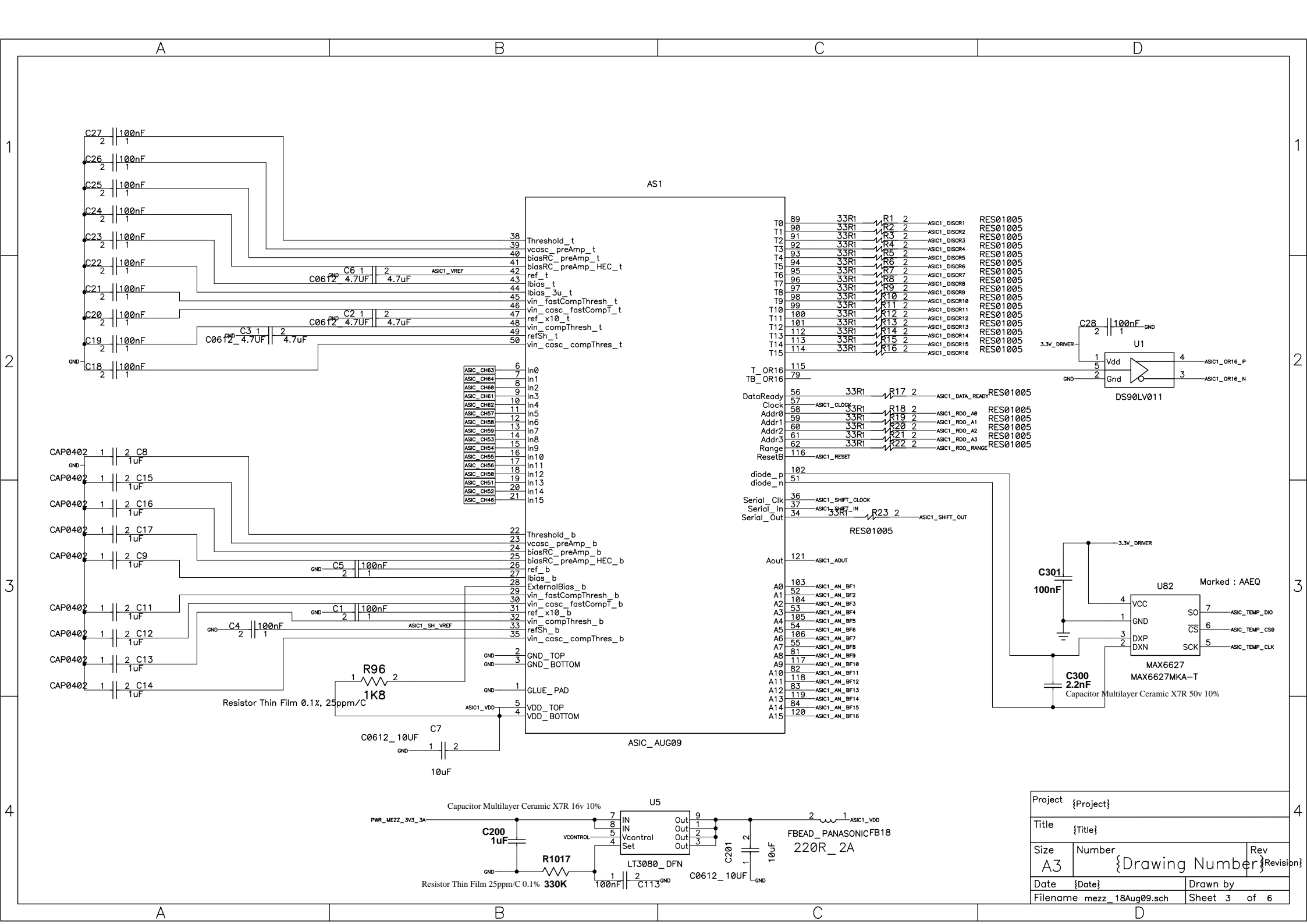
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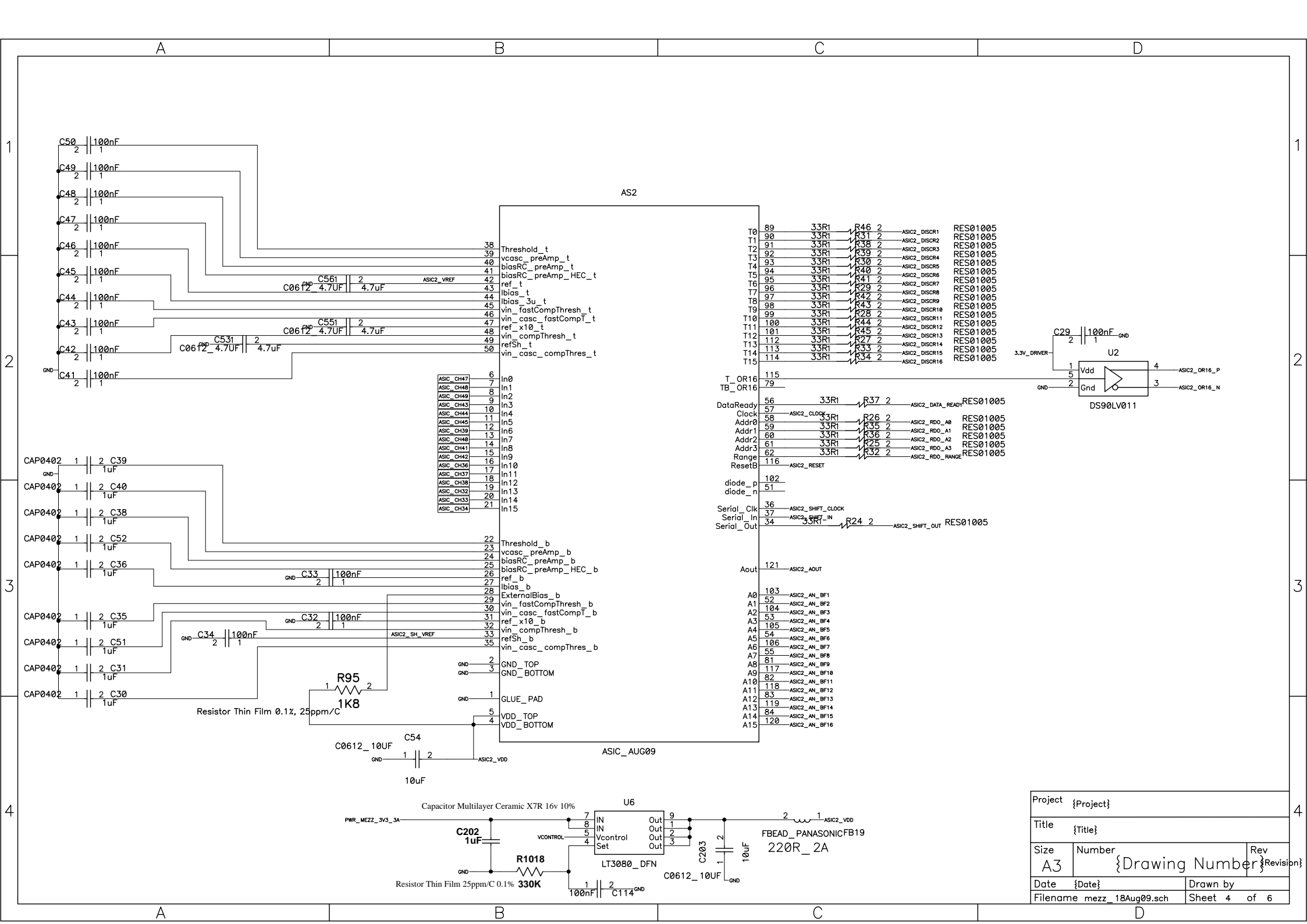
B

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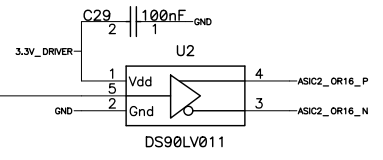
AS2

6	In0	ASIC_CH47
7	In1	ASIC_CH48
8	In2	ASIC_CH49
9	In3	ASIC_CH43
10	In4	ASIC_CH44
11	In5	ASIC_CH45
12	In6	ASIC_CH39
13	In7	ASIC_CH40
14	In8	ASIC_CH41
15	In9	ASIC_CH42
16	In10	ASIC_CH36
17	In11	ASIC_CH37
18	In12	ASIC_CH38
19	In13	ASIC_CH32
20	In14	ASIC_CH33
21	In15	ASIC_CH34

22	Threshold_b	
23	vcasc_preAmp_b	
24	biasRC_preAmp_b	
25	biasRC_preAmp_HEC_b	
26	ref_b	
27	lbias_b	
28	ExternalBias_b	
29	vin_fastCompThresh_b	
30	vin_casc_fastCompT_b	
31	ref_x10_b	
32	vin_compThresh_b	
33	refSh_b	
35	vin_casc_compThres_b	

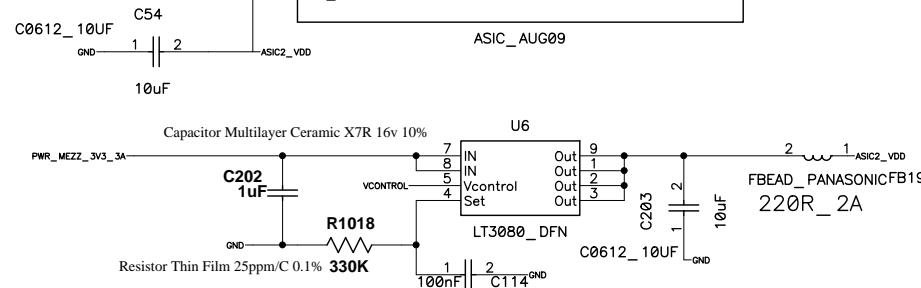
2	GND_TOP	
3	GND_BOTTOM	
1	GLUE_PAD	
5	VDD_TOP	
4	VDD_BOTTOM	

T0	89	33R1	R46	2	ASIC2_DISCR1	RES01005
T1	90	33R1	R31	2	ASIC2_DISCR2	RES01005
T2	91	33R1	R38	2	ASIC2_DISCR3	RES01005
T3	92	33R1	R39	2	ASIC2_DISCR4	RES01005
T4	93	33R1	R30	2	ASIC2_DISCR5	RES01005
T5	94	33R1	R40	2	ASIC2_DISCR6	RES01005
T6	95	33R1	R41	2	ASIC2_DISCR7	RES01005
T7	96	33R1	R29	2	ASIC2_DISCR8	RES01005
T8	97	33R1	R42	2	ASIC2_DISCR9	RES01005
T9	98	33R1	R43	2	ASIC2_DISCR10	RES01005
T9	99	33R1	R28	2	ASIC2_DISCR11	RES01005
T10	100	33R1	R44	2	ASIC2_DISCR12	RES01005
T11	101	33R1	R45	2	ASIC2_DISCR13	RES01005
T12	112	33R1	R27	2	ASIC2_DISCR14	RES01005
T13	113	33R1	R33	2	ASIC2_DISCR15	RES01005
T14	114	33R1	R34	2	ASIC2_DISCR16	RES01005

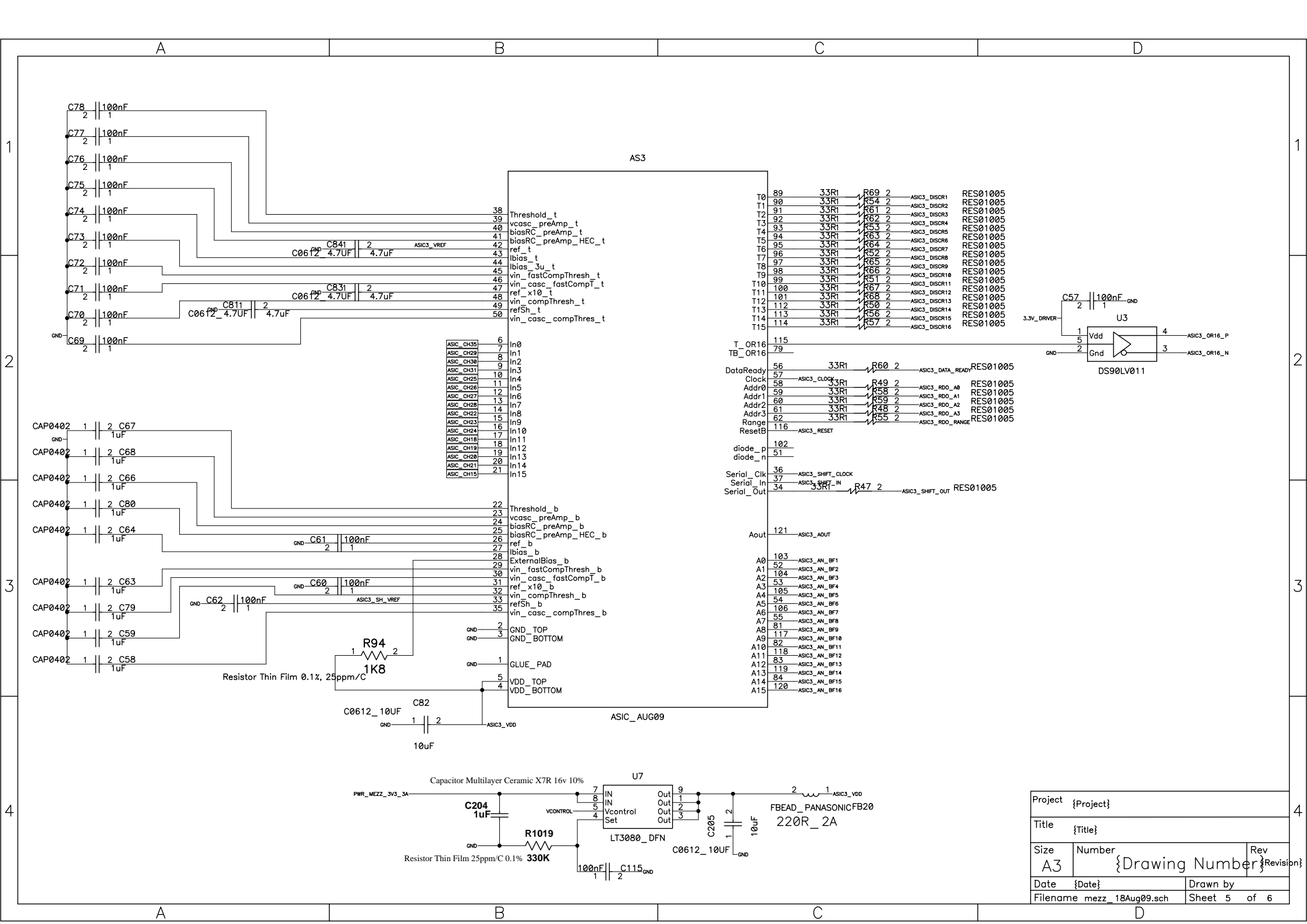


T15	115	33R1	R37	2	ASIC2_DATA_READY	RES01005
T15	116	33R1	R26	2	ASIC2_RDO_A0	RES01005
T15	117	33R1	R35	2	ASIC2_RDO_A1	RES01005
T15	118	33R1	R36	2	ASIC2_RDO_A2	RES01005
T15	119	33R1	R37	2	ASIC2_RDO_A3	RES01005
T15	120	33R1	R32	2	ASIC2_RDO_RANGE	RES01005
T15	121	33R1	R24	2	ASIC2_SHIFT_OUT	RES01005

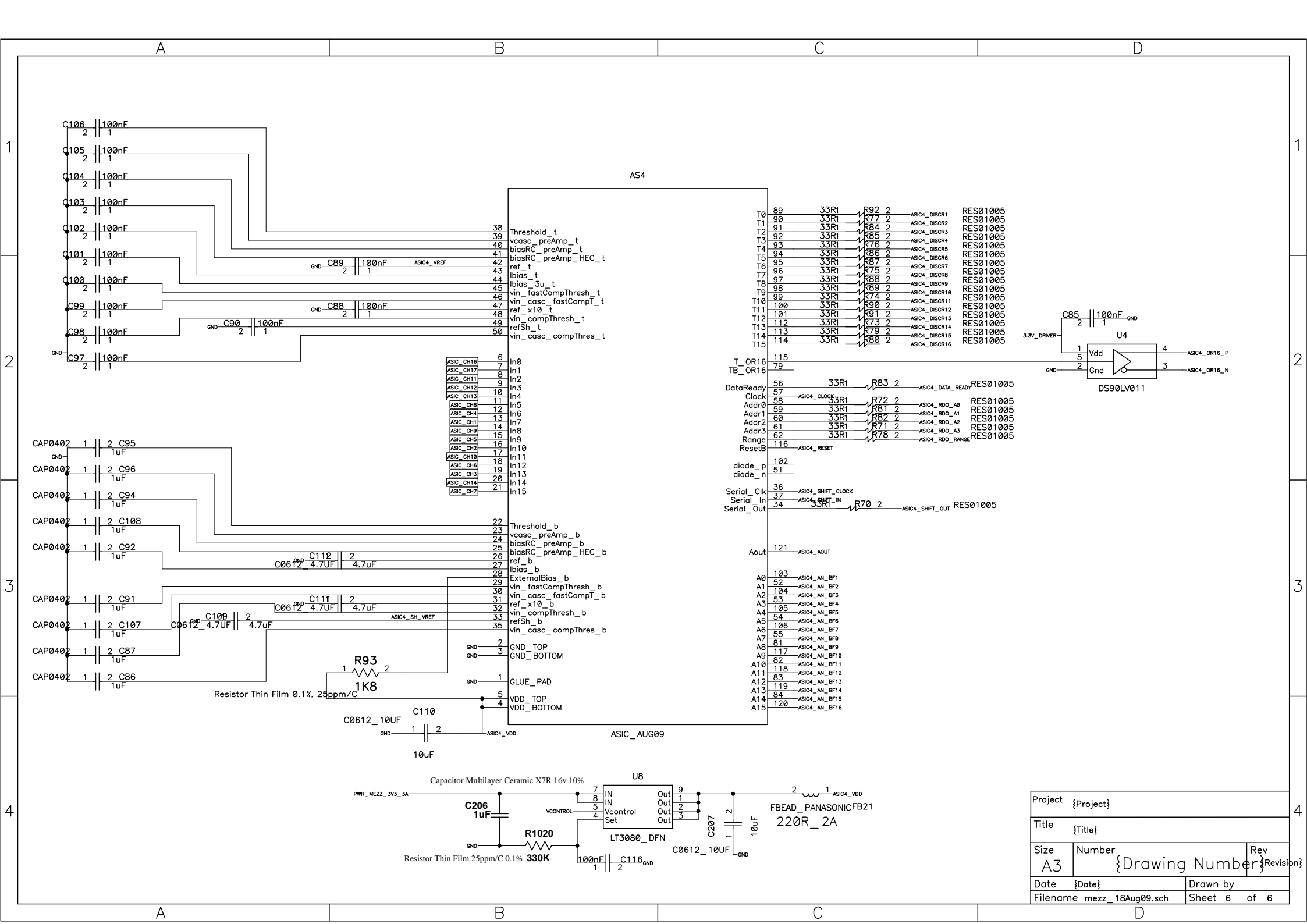
A0	103	ASIC2_AN_BF1
A1	52	ASIC2_AN_BF2
A2	104	ASIC2_AN_BF3
A3	53	ASIC2_AN_BF4
A4	105	ASIC2_AN_BF5
A5	54	ASIC2_AN_BF6
A6	106	ASIC2_AN_BF7
A7	55	ASIC2_AN_BF8
A8	81	ASIC2_AN_BF9
A9	117	ASIC2_AN_BF10
A10	82	ASIC2_AN_BF11
A11	118	ASIC2_AN_BF12
A12	83	ASIC2_AN_BF13
A13	119	ASIC2_AN_BF14
A14	84	ASIC2_AN_BF15
A15	120	ASIC2_AN_BF16



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