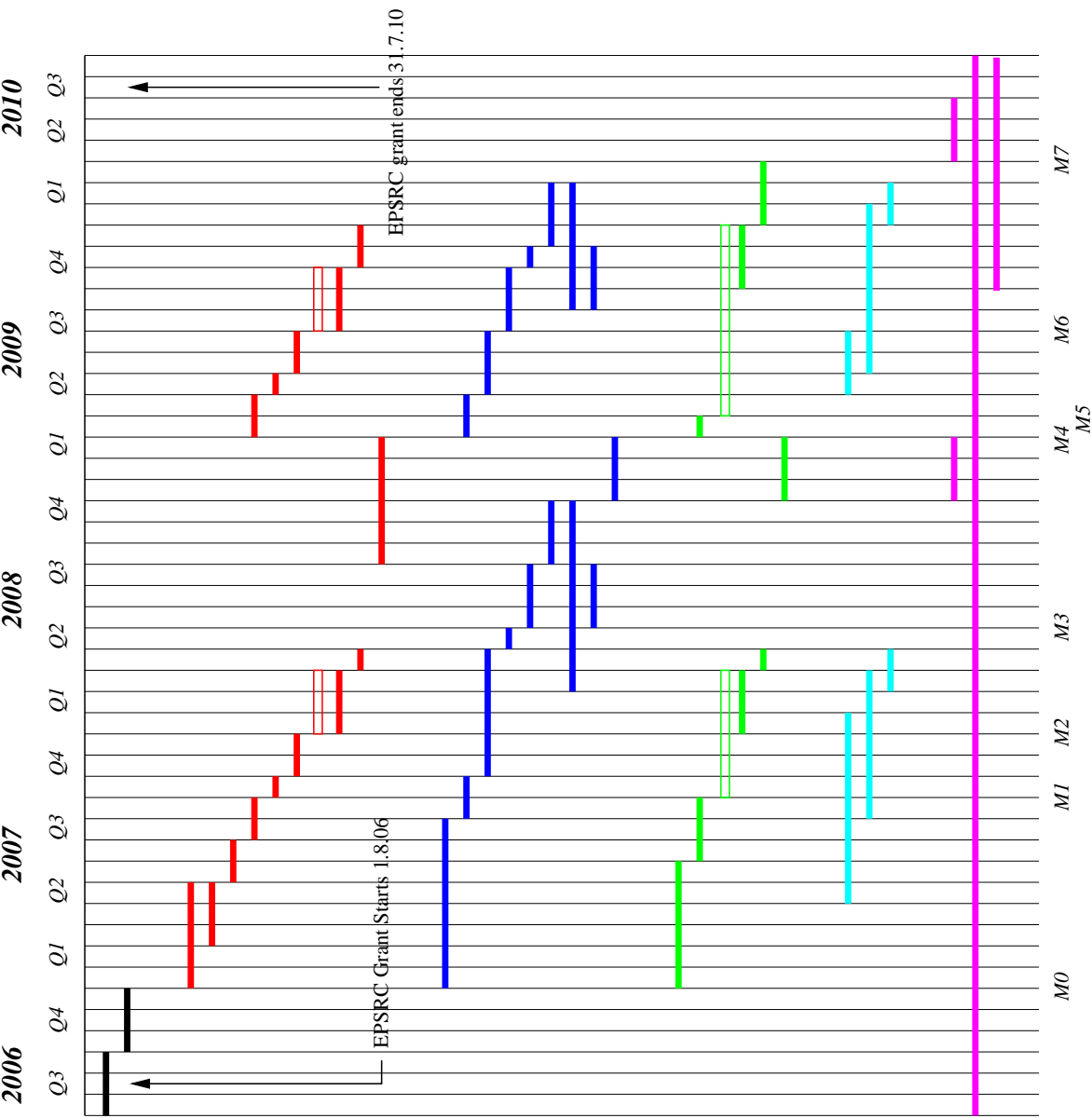


# Workplan

## Decay Spectroscopy of Exotic Nuclei at FAIR Edinburgh/Liverpool/CCLRC Daresbury & RAL



Design Study Review

### ASIC

Analogue design  
Digital design  
Design Integration  
Layout & Functional Verification  
DRC  
Design Review  
AMS MPW Submission  
Acceptance Test Preparation  
Acceptance Tests  
Design Modifications

### FEE

Design  
Layout & Functional Verification  
FPGA Firmware  
PCB fabrication & assembly  
Systems Integration  
Functional Tests  
Data Acquisition Software  
Slow Control Software  
Design Modifications

### DSSD

Design  
Design Review  
Production  
Acceptance Test Preparation  
Acceptance Tests  
Design Modifications

### Mechanical

Design  
Manufacture  
Assembly

Commissioning Tests  
Ongoing Experimental Programme  
Proposal Preparation for FAIR/DESPEC/DSSD

### Milestone

- M0 System design specification approved: Edinburgh/Liverpool/CCLRC DL & RAL/DESPEC
- M1 DSSD prototype production approved
- M2 MPW submission of prototype ASIC approved
- M3 Prototype DSSD, ASIC & FEE available for systems integration
- M4 Commissioning tests of prototype integrated DSSD, ASIC & FEE completed
- M5 DSSD production approved: Edinburgh/Liverpool/CCLRC DL & RAL/DESPEC
- M6 MPW submission of production ASIC approved: Edinburgh/Liverpool/CCLRC DL & RAL/DESPEC
- M7 Production DSSD, ASIC & FEE available for commissioning tests